



Ultra 37000 CPLD Pinout Table for all Device Packages

Package ->	TQFP44	PLCC44	FBGA48	PLCC84	TQFP100	FBGA100	TQFP160	TQFP208	F/BGA256	BGA352	FBGA400
Signal	CY37032 CY37064 CY37032V CY37064V	CY37032 CY37064	CY37032V CY37064V	CY37064 CY37128	CY37064 CY37128 CY37256 CY37064V	CY37064V CY370128V	CY37128 CY37192 CY37512 CY37192V CY37256V	CY37256 CY37384 CY37512 CY37256V CY37384V CY37512V	CY37256 CY37384 CY37256V CY37384V CY37512V	CY37512V CY37512V	CY37512V
TMS	13	19	F1	35	26	H2	46	59	L5/Y4	AF7	N7
TDI	33	39	A8	72	75	C9	116	150	F12/D19	E24	H14
TCK	1	7	A1	14	1	C2	6	7	F5/E2	E2	H7
TDO	21	27	F8	51	50	H9	76	98	L12/V16	AF20	N14
JTAGEN	5		D1	83	-	-/F2	-/139	-	-	-	-
PULLUP											

Title		
ADC-CY37xxx for HiLo ALLxx		
Size	Number	Revision
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File:	C:\HiLo\ADP-CY37xxx\SchDoc	Drawn By: tinhead@eevblog