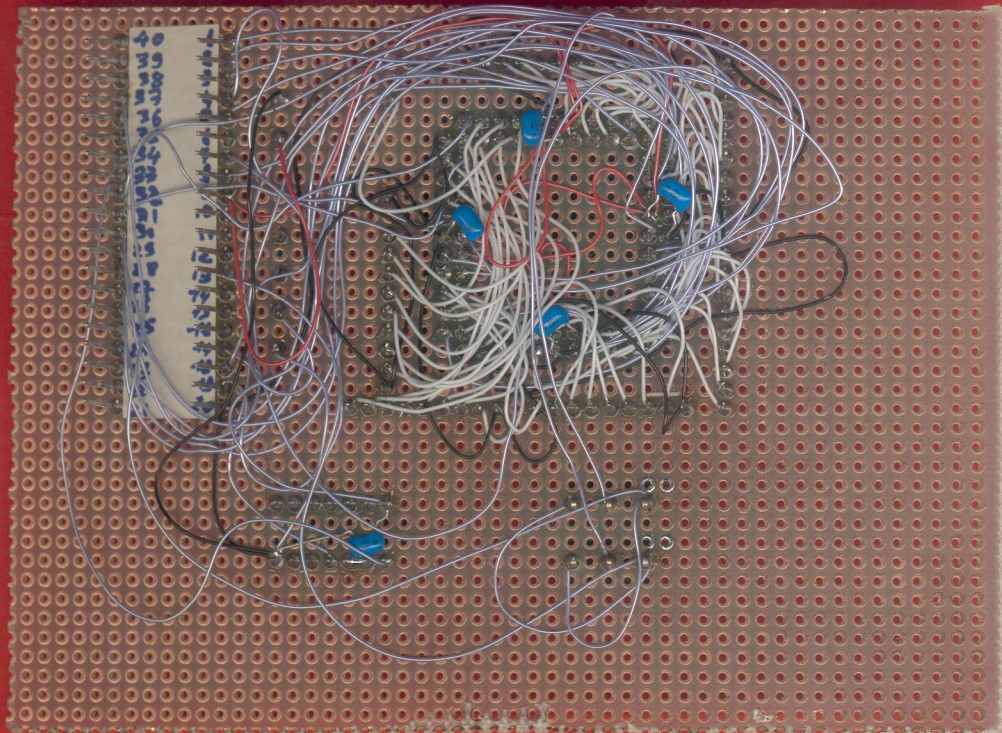
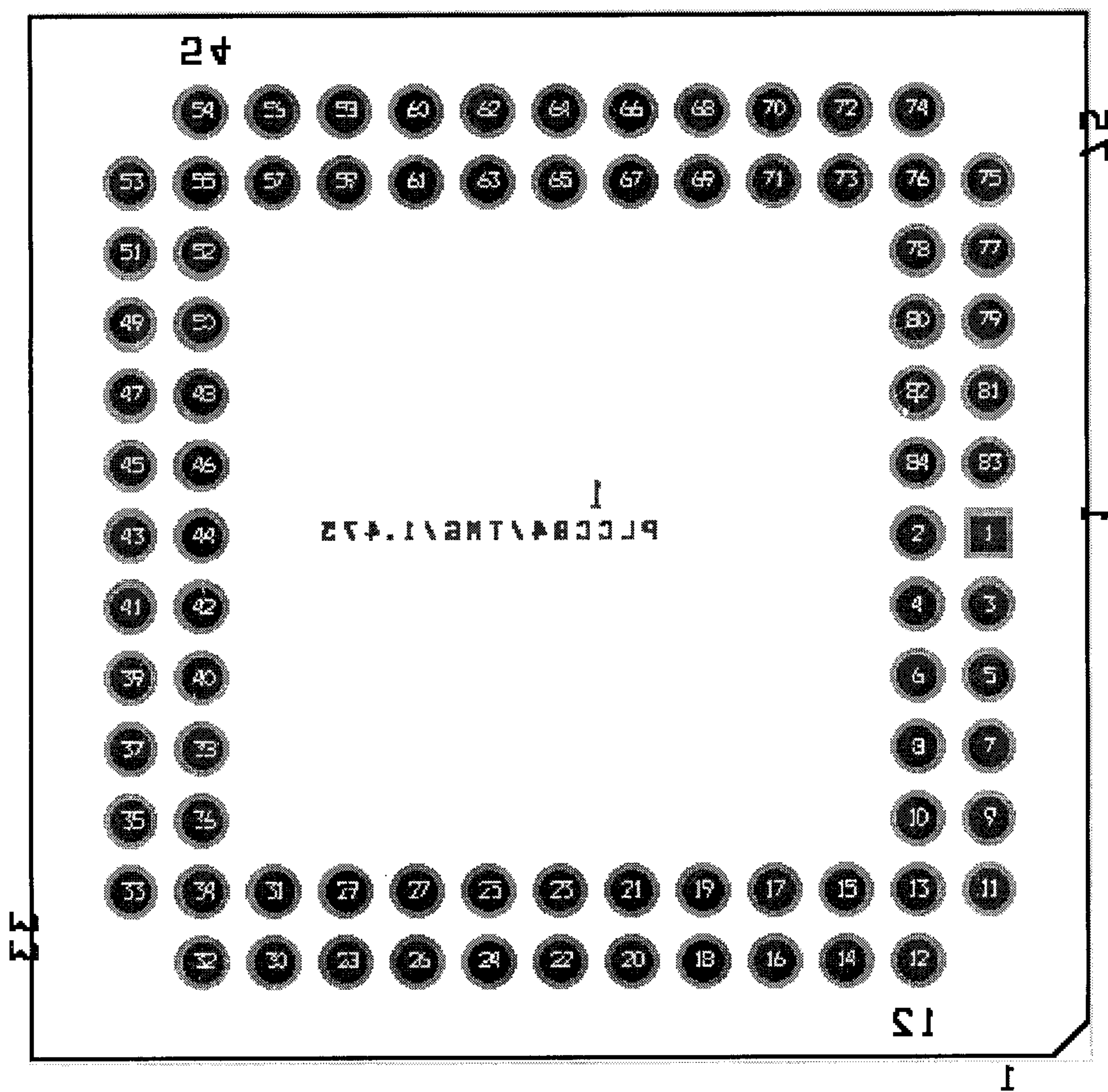
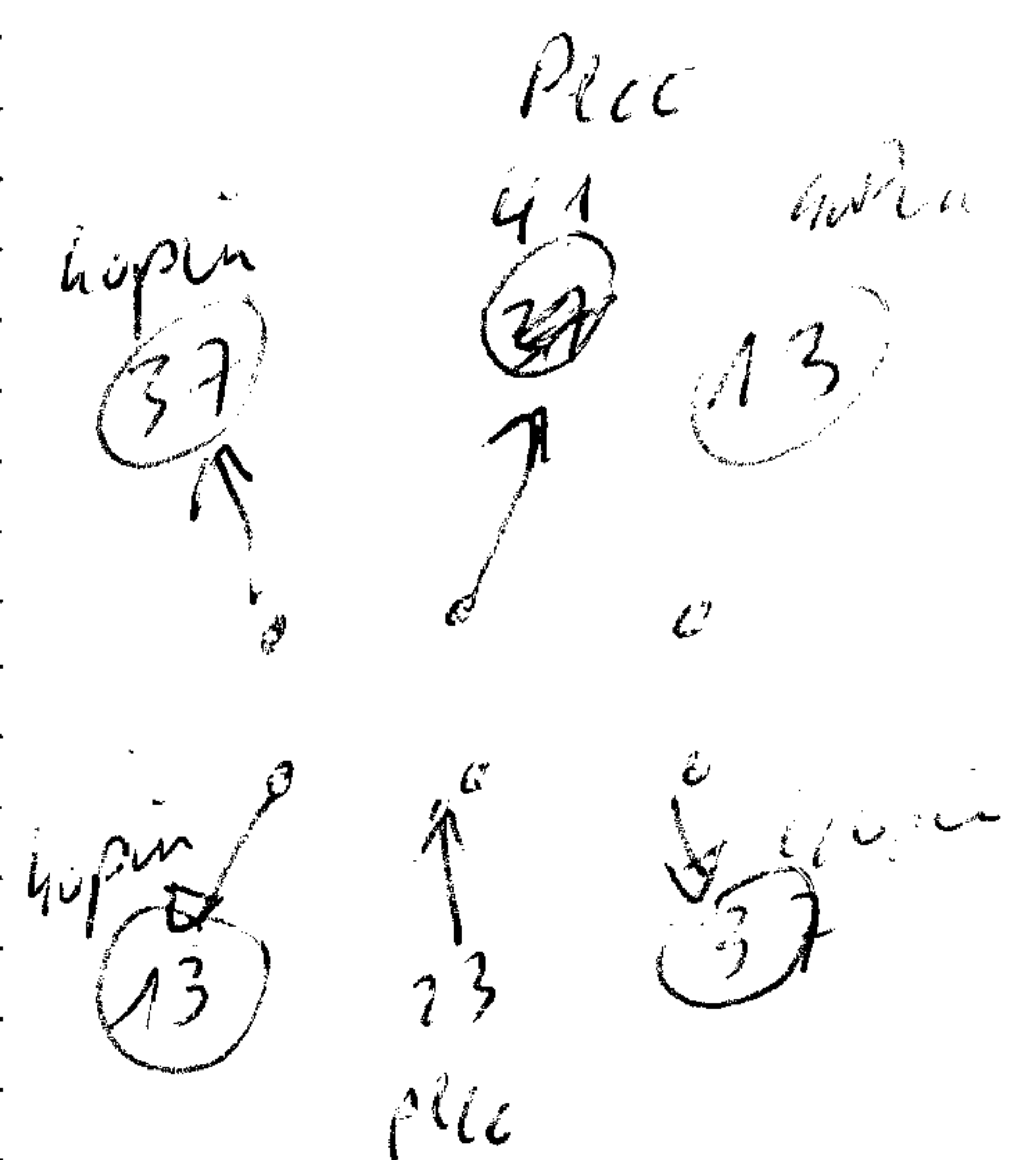
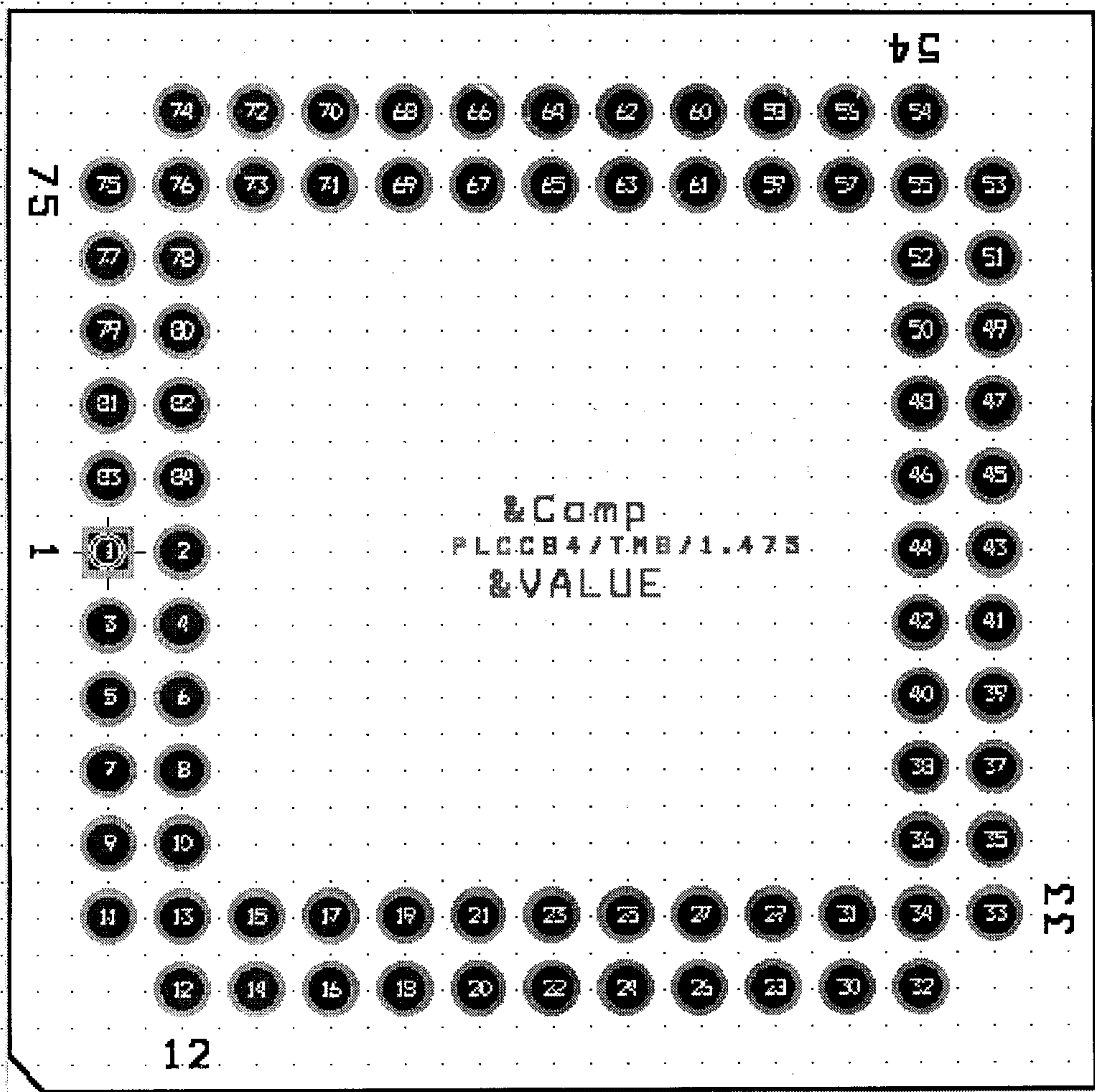








&Comp

DIP40

PLCCB4

pin 7 — pin 14 du 74LS14

pin 10 — pin 7 du 74LS14
common réseaux 10K

RP1

pin 33 — pin 1 du 74LS14.
pin 2 et 3 du 74LS14 reliés ensemble

— pin 4 du 74LS14 — pin 10
10nF entre 20 (GND) et pin 13 du DIP40

C2 100nF (pin 14, 7 du 74LS14)

C3 100nF (pin 9, pin 20) →

C7 100nF (VCC GND)

C4 100nF

C5 100nF

C6 100nF

C1 10µF (35V)

(pin 9, pin 20)
(+)(-)

à compléter

1, 2

21, 22

42, 43

65, 64

10nF entre pin 66 et 64 (on)

84 Pin PLCC Socket	Signal Name	Pins referenced to 28 Pin DIL BASE 40		48 Pin DIL BASE	Comments
1	GND	20	14	24	
2	VCC	9	28	38	
3	10K Pulldown R		-	-	
4	10K Pulldown R		-	-	
5	10K Pulldown R		-	-	
6	10K Pulldown R		-	-	
7	10K Pulldown R		-	-	
8	10K Pulldown R		-	-	
9	10K Pulldown R		-	-	
10	SI/O7	25	16	26	
11	GND	20	14	24	
12	SI/O8	26	17	27	
13	10K Pulldown R		-	-	
14	10K Pulldown R		-	-	
15	10K Pulldown R		-	-	
16	10K Pulldown R		-	-	
17	10K Pulldown R		-	-	
18	10K Pulldown R		-	-	
19	SI/O15	1	7	17	
20	CLK1	7 74LS14	4	11	
21	VCC	9	28	38	
22	GND	20	14	24	

23	VPP	13	2	12	
24	SI/O16	2	8	18	
25	SI/O17	5	9	19	
26	10K Pulldown R		-	-	
27	10K Pulldown R		-	-	
28	10K Pulldown R		-	-	
29	10K Pulldown R		-	-	
30	10K Pulldown R		-	-	
31	SI/O23	27	18	28	
32	GND	20	14	24	
33	SI/O24	8	19	29	
34	SI/O25	23	15	25	
35	10K Pulldown R		-	-	
36	10K Pulldown R		-	-	
37	10K Pulldown R		-	-	
38	SEC	19	12	22	
39	10K Pulldown R		-	-	
40	10K Pulldown R		-	-	
41	ENABLE	37 + 8 sw. h. h. sw. 1		13	
42	VCC	9	28	38	
43	GND	20	14	24	
44	VCC	9	28	38	
45	10K Pulldown R		-	-	
46	10K Pulldown R		-	-	

47	10K Pulldown R	-	-	-
48	10K Pulldown R	-	-	-
49	10K Pulldown R	-	-	-
50	10K Pulldown R	-	-	-
51	10K Pulldown R	-	-	-
52	SI/O39	29	20	30
53	GND	20	14	24
54	SI/O40	30	21	31
55	10K Pulldown R	-	-	-
56	10K Pulldown R	-	-	-
57	10K Pulldown R	-	-	-
58	10K Pulldown R	-	-	-
59	10K Pulldown R	-	-	-
60	10K Pulldown R	-	-	-
61	SI/O47	3	10	20
62	CLK2	34	4	14
63	VCC	9	28	38
64	GND	20	14	24
65	MD1	17	5	15
66	SI/O48	4	11	21
67	10K Pulldown R	-	-	-
68	10K Pulldown R	-	-	-
69	10K Pulldown R	-	-	-

70	10K Pulldown R	-	-	
71	10K Pulldown R	-	-	
72	10K Pulldown R	-	-	
73	SI/O55	31 27	32	
74	GND	20 14	24	
75	SI/O56	32 23	33	
76	10K Pulldown R	-	-	
77	10K Pulldown R	-	-	
78	10K Pulldown R	-	-	
78	10K Pulldown R	-	-	
79	10K Pulldown R	-	-	
80	10K Pulldown R	-	-	
81	10K Pulldown R	-	-	
82	10K Pulldown R	-	-	
83	MD2	18 6	16	
84	VCC	9 28	38	
		13	23	
		24	34	
		25	35	
		26	36	
		27	37	
		All other pins nc	All other pins nc	

1. A capacitor (eg 10nF) is normally fitted to bypass VCC supply. We can supply the adapter without this adapter fitted on request

2.A 1 μ f tantalum capacitor should be put between VCC and GND.