

1. DYNAMIC RAM (4/5)

Structure	Total Bit	Organization	Number of Pin	Ok	Hitachi	Intel	Texas	Motorola	NEC	Toshiba	Mitsubishi	Fujitsu
CMOS	1M	262,144 ×4	20	MSM514256A-80	HM514256A-8				μPD424256-80	TC514256A-80	M5M44256A-8	MB81C4256-80
				80 P.J.Z	80 P.J.P.ZP				80 C.LA.V	80 P.J.Z	80 P.J.L	80 C.P
				MSM514256A-10	HM514256A-10				μPD424256-10	TC514256A-10	M5M44256A-10	MB81C4256-10
				100 P.J.Z	100 P.J.P.ZP				100 C.LA.V	100 P.J.Z	100 P.J.L	100 C.P
				MSM514258A-80	HM514258A-8				μPD424258-80	TC514258A-80	M5M44258A-8	MB81C4258-80
				80 P.J.Z	80 P.J.P.ZP				80 C.LA.V	80 P.J.Z	80 P.J.L	80 C.P
				MSM514258A-10	HM514258A-10				μPD424258-10	TC514258A-10	M5M44258A-10	MB81C4258-10
				100 P.J.Z	100 P.J.P.ZP				100 C.LA.V	100 P.J.Z	100 P.J.L	100 C.P
NMOS	2048k	262,144 ×8	30	MSC2304-10								
				100 YS.KS								
				MSC2304-12								
				120 YS.KS								
				MSC2304-15								
				150 YS.KS								
	2304k	262,144 ×9	30	MSC2304-10								
				100 YS.KS								
				MSC2304-12	HM561003-12						MH25609-12	
				120 YS.KS	120 YS.KS						120 YS	
				MSC2304-15	HM561003-15						MH25609-15	
				150 YS.KS	150 YS.KS						150 YS	
		262,144 ×8	30	MSC2307-10								
				100 YS.KS								
				MSC2307-12								
				120 YS.KS								
				MSC2307-15								
				150 YS.KS								
	2304k	264,144 ×9	30	MSC2307-10								
				100 YS.KS								