

Differences between W77C32/E58 and DS80C320/520

- On-chip ROM size: 32KB (W77E58) 16KB (DS80C520)
- External Interrupts: 2 (WB) 6 (DS)
- Encryption Feature: Yes (WB) No (DS)
- Bandgap Voltage Reference(**): No (WB) Yes (DS)
- Instruction timing (machine cycle*) improvements:

WB:DS

INC DPTR 2:3

DEC DPTR 2:x

RET (RETI) 2:4

JMP @A+DPTR 2:3

*One machine cycle = 4 Clocks

** For power-fail reset & interrupt

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