Magnetic Memories to TTL Systems (continued)

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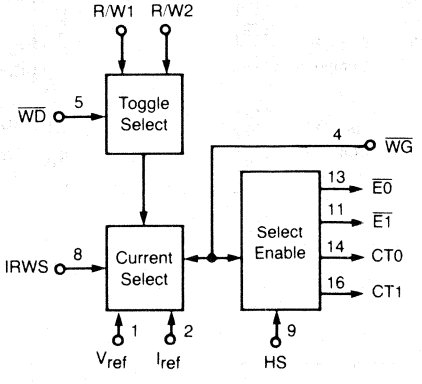
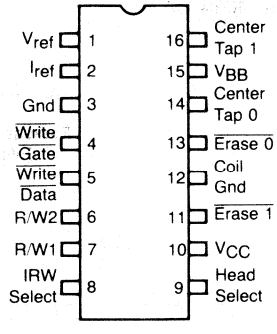
FLOPPY DISK WRITE CONTROLLER/HEAD DRIVER SYSTEMS

MC3469P (Straddle Erase) — is designed to provide the entire interface between floppy disk heads and the head control and write data signals for stradle-erase heads.

Provisions are made for selecting a range of accurately controlled write currents and for head selection during both read and write operation. Additionally, provisions are included for externally adjusting degauss period and inner/outer track compensation.



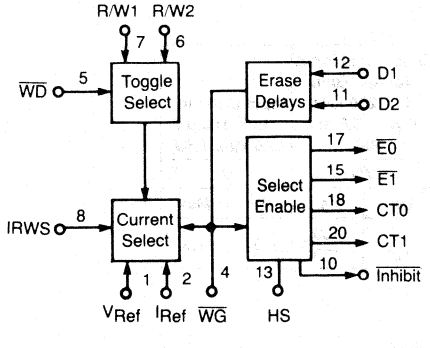


MC3471P (Tunnel Erase) — is designed to provide the entire interface between the write data and head control signals and the heads (write and erase) for either Tunnel or straddle-erase floppy disk systems.

Provisions are made for selecting a range of accurately controlled write currents and for head selection during both read and write operation. Additonally, provisions are included for externally adjusting degauss period, inner/outer track compensation, and the delay from write gate to erase turn-on and turn-off.

Erase Delays are controlled by driving the delay inputs D1 and D2 with standard TTL open-collector logic (microprocessor compatible) or by using the external RC mode in which case the delay is one τ (K factor = 1.0).

In addition, a Inhibit output is provided which indicates that the heads are active during write, degauss, or erase.



T_A = 0 to +70°C

Packages:
MC3469P — Case 701
MC3471P — Case 738