

USER DEFINED LOGIC HANDBOOK

EPLD VOLUME

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About Our Cover:

The design on our front cover is an abstract portrayal of the erasability and programmability of Erasable Programmable Logic Devices (EPLDs).

EPLDs are manufactured as unprogrammed CHMOS large scale integration logic circuits that a user customizes through programming on a logic development system. EPLDs are easy-to-use logic devices that speed up the logic design process, thereby enabling the user to shorten time-to-market efforts.

EPLDs typify Intel's commitment to producing sophisticated design tools which deliver low risk custom design solutions.



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TABLE OF CONTENTS

Alphanumeric Index	iv
CHAPTER 1	
Overview Introduction to Intel EPLDs	1-1
CHAPTER 2 EPLDs (Erasable Programmable Logic Devices)	
DATA SHEETS	
5C031/5C032, 300-Gate CHMOS H-Series Erasable Programmable Logic	2-1
5C060/5C090, 600-/900-Gate CHMOS H-Series Erasable Programmable Logic Device (H-EPLD)	2-6
5C121, 1200-Gate CHMOS H-Series Erasable Programmable Logic Device	2-25
5C180, 1800-Gate CHMOS Erasable Programmable Logic Device	2-39
M5C060, Military 600-Gate CHMOS H-Series Erasable Logic Device	2-40
APPLICATION BRIFFS	
AR-8 Implementing Cascaded Logic in the 5C121	2-58
AB-9 5C121 as a Three And One Half Digit Display Driver	2-63
AB-10 Square Pegs in Round Holes—A Fitting Tutorial for the 5C121	2-68
AB-11 16-Bit Binary Counter Implementation Using the 5C060 EPLD	2-80
AB-12 Designing A Mailbox Memory for Two 5C031's	2-105
AB-13 TTL Library for EPLD Design	2-103
APPLICATION NOTES AP-271 Applying the 5C121 Architecture	2-111
AP-271 Applying the 5C121 Architecture	2-123
AP-276 Implementing a CMOS Bus Arbiter/Controller in the 5C060 EPLD	2-134
TECHNICAL PAPERS	
Techniques for Modular EPLD Designs	. 2-144
CHAPTER 3	
Development Support Tools DATA SHEETS	
iPLDS, Intel Programmable Logic Development System	. 3-1
APPLICATION NOTES AP-279 Implementing an EPLD Design Using Intel's Programmable Logic	
Development System	. 3-9
CHAPTER 4	
Annendiy	
Intel FPI D Numbering Scheme	. 4-1
List of Compatible PCs for iPLDS	. 4-3
Device Feature Comparison	. 4-4
EPLD Gate Counting Methodology	. 4-5

ALPHANUMERIC INDEX

5C031/5C032, 300-Gate CHMOS H-Series Erasable Programmable Logic Device	
(H-EPLD)	2-1
5C060/5C090, 600-/900-Gate CHMOS H-Series Erasable Programmable Logic	
Device (H-EPLD)	2-6
5C121, 1200-Gate CHMOS H-Series Erasable Programmable Logic Device	2-25
5C180, 1800-Gate CHMOS Erasable Programmable Logic Device	2-39
M5C060, Military 600-Gate CHMOS H-Series Erasable Logic Device	2-40
iPLDS, Intel Programmable Logic Development System	3-1