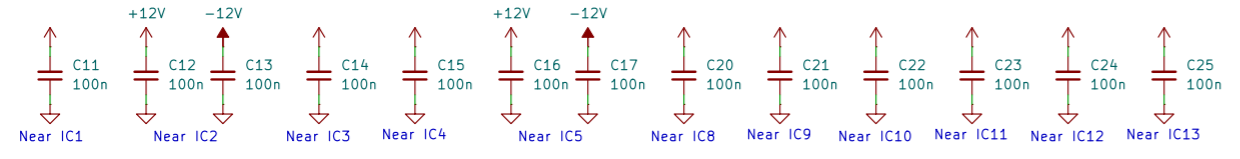


default	ON	Port
SW4	IRQ4 (COM1/3) *	
SW5	IRQ3 (COM2/4)	Serial port 1
SW6	IRQ4 (COM1/3)	
SW7	IRQ3 (COM2/4) *	Serial port 2
SW8	IRQ7 (LPT1) *	
SW9	IRQ5 (LPT2)	Parallel port

	ON	OFF
SW1	LPT1	LPT2
SW2	COM1/2	COM3/4
SW3	2x COM	1x COM

R9 pull-up added to avoid floating CE on 74LS245 during DMA (AEN=-1).



This schematic has been reconstructed for the purpose of study and technical analysis. During this process, a design issue was identified and corrected in the present version of the schematic (R9 pull-up added to avoid floating CE on 74LS245 during DMA). The original design remains the intellectual property of «Flytech Technology Co Ltd».

Olivier DENIZOT
 Sheet: /
 File: Board.kicad_sch

Title: (a)AT PB-AT003/C (corrected)

Size: A3	Date: 2026-04-30	Rev: 1.0
KiCad E.D.A. 10.0.1		Id: 1/1