

elan

E11



E11 EPROM SIMULATOR ADAPTER

- SIMULATES ALL STANDARD JEDEC EPROMS UP TO 27128 I.E. 2716, 2732, 2764, 27128. (NMOS, HMOS, CMOS).
- POWERED FROM PROGRAMMER OR TARGET SYSTEM.
- OPTIONAL EXPANSION FOR 27256.
- CAN BE PARALLELED FOR 16 BIT OR TWIN EPROM SIMULATION.
- TYPICAL ACCESS TIME 175N SEC.
- APPROVED BY LEADING DEVICE AND SYSTEMS MANUFACTURERS.
- BATTERY BACK UP TO SAVE DATA.



Eprom expertise

The E11 Adapter provides in-circuit EPROM Simulation in conjunction with an E or EA Series editing programmer. Both the programmer and simulator personality control is via software. Target system EPROM data is stored in fast CMOS static RAM, being downloaded via the parallel port of the programmer. Simulation is easily halted, data changed and then restarted. Data can be saved and the simulator run when disconnected from the programmer. The RAM is backed up with a Ni-Cad battery, charged by the programmer or the target system. Current drain of the simulator is less than the EPROM it replaces. A hardware RESET or RESET line is provided to prevent hardware crashes at switch on or off. Most microprocessor ROM speed requirements can be handled with the access time of typically 175nSec. Two EPROMS, either in 8 bit or 16 bit configuration, may be simulated simultaneously by running two E11 units in parallel.

The simulator is packaged in a welded steel case to give excellent protection and shielding.

SPECIFICATIONS

Dimensions: 180 × 150 × 67mm (approx.)

Weight: ½Kg

Temperature: 15–35°C ambient

Relative Humidity: 0–90% non-condensing

Emulation Sockets & Cables: Cable is shielded, 475mm long, and is provided with 24 and 28 pin headers.

We reserve the right to change the specifications, functions and circuitry without notice.



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