15.6.88

ELAN DIGITAL SYSTEMS LTD

BULLETIN No. 31

In General

Programmability:

The new issue of Programmability is enclosed with this bulletin together with an additions sheet showing the changes included since the last quarter.

A preliminary issue of Programmability is also enclosed which is formatted to show the target first issue coverage of the new 3000, 4000 and 5000 programmer range, together with the Universe P & G Stacks and the continuing production of the E8B.

Revision Levels:

A note of current firmware and manual revision levels is included with this bulletin for your convenience.

Flash Approval:

Another first for ELAN!!!!

We have received INTEL 27F64 approval for Universe 1013/G-Stack.

Competitive Information

Stag PPZ Universal Programmer.

One of our UK customers has been evaluating Universal Programmers and has chosen our Universe 1000 in preference to the PPZ. The main reasons given were:

- PPZ is limited to 512K bit devices
- No auto sensing of the number of devices present in the ZIF Sockets
- No 'chaining' cross reference between similar devices across the manufacturer ranges on the display
- The unit has to be dismantled to fit update firmware
- Access Time testing requires use of a "Scope"
- PPZ is not user friendly - it cannot be used without constant reference to the manual

Please use this against your next PPZ confrontation!

Technical Update

1. Universe 1013/G-Stack Set/Gang (8 x 32 pin) EPROM Programmer.

Firmware Release G3.21

Introduction of Intel 27F64 Flash EPROM.

Programs with the Quick-Pulse algorithm

Erases with an interactive Flash Erase algorithm allowing erasure within about 7 seconds

NOTE: Flash Erase is provided on the 1st socket only.

2. Universe 1000 - MCU

Firmware Release M3.28

This release enables the erase "E" key on the MCU keypad. For G-Stack selection of Intel 27F64 Flash EPROM -use 1st socket only.

Patrick Goss
Sales and Marketing Support Manager.

Stop Press:

Please see enclosed Device Priority Listings showing our plans for new device support.

CURRENT MANUAL & FIRMWARE REVISION LEVELS.

CURRENT FIRMWARE REVISION LEVELS 22-6-88:

C41	C13 (C14A WHEN E13 ADAPTER REQUIRED)
E12C	ESB20A
E9C	EB10
ESB	EB10
1013/G-STACK	G3.21
1012/P-STACK	P3.27
1011/L-STACK	L3.28
1014/F-STACK	F3.02

MASTER CONTROL UNIT (MCU) M3.28

CURRENT MANUAL REVISION LEVELS 15-3-88

E12C REV	1.1
EB SERIES	1.92
C41	1.84
E9C	1.2
MCU	2.1
L-STACK 1011	2.0
P-STACK 1012	2.0
G-STACK 1013	2.1
F-STACK 1014	1.1
EASYCOM	1.1

DEVICE PRIORITY LISTING continued

The remaining devices are listed in priority order.

Lattice 39V18
Sharp EPROM family
28256 EEPROM

Motorola 68764/68766

Cypress 7C245
7C256
7C261
7C263
78C800

ICT 27CX641 27CX642

TI 2564

SGS PALs

Signetics PLS155 PLS129

We shall work to this list and expect your continuing enquiries and requests for devices to help us maintain an up to date view of your popular requirements.

The list will be repeated in subsequent Bulletins to show our progress in implimentation and to track our priorities.

DEVICE PRIORITY LISTING

In response to your feedback at the May conference we have set up a target release priority for implementation of NEW DEVICES as follows:-

1. Microprocessors Incorporating EPROM — DATE

   New series ZIFPAC options for 3000/4000/5000 programmers.

   a. Single 40 pin socket covering Intel 8751/8752 series:-

      8751, 8751, 87C51, 8752A, 8752BH. — END SEPT. 88

   b. Single 40 pin socket supporting Motorola 68701/68705 series — END OCT88

   c. Single 40 pin socket covering Intel/NEC 8748/9 & 8755A series:-
      8741, 8742A, 8748, 8748H, 8749H, 8755A. — END OCT. 88

   d. 2 X 40 pin sockets supporting both the Intel 8748/8751 series. — END OCT. 88

2. Electrically Erasable PALs/GALs

   Universe 1014/F-Stack EPLD & CMOS PAL programmer.

   Lattice (and equivalent) 16V8, 20V8 — END SEPT. 88

3. CMOS PALs

   Universe 1014/F-Stack EPDL & CMOS PAL programmer.

   AMD — C22V10, 22VF10, 32V X 10, HC29M16 — NOV 88

   Altera — EP1800 — DEC 88