26-06-89

ELAN DIGITAL SYSTEMS LTD

Bulletin No. 39

In General

Programmability:

Enclosed please find latest additions sheet showing the changes since last issue (20-04-89).

Revision Levels:

Please note new revision levels on following

• 3000-4000-5000 machines	3.03
• Universe MCU	4.02
• L stack	L3.33

(see technical update for details)

Lattice:

16V8A and 20V8A GAL software shortly available. I hope to announce these in the next bulletin.

QUALIFICATIONS:

We are extremely pleased to announce that we have received the following qualifications:

- INTEL - 5-142 programmer for all Intel devices presently supported
- LATTICE - 1014 Stand Alone EPLD programmer for Lattice, National and SGS Thompson GALS.

Please see attached approvals.

Technical Update

3000-4000-5000 series

New 3.03 software now available for SGS 27C64A and SGS 27C1024 devices and National Semiconductor's NMC27C010.

Plus enhanced diagnostics on all aspects of programmer. This also includes algorithm changes to National 27C32 device.

We now have the technology to enable those customers who require it to program Intel Flash 28F010 and 28F512 devices.

To enable the Intel Flash devices to be done, existing ZIFPACs need to be upgraded with new PALs. When ordering new software please order PALs as detailed and don't forget current revision levels of your machine.

EASYCOM 2

This is now only available for the UNIVERSE and the old E series.

For remote contro of the EF-PER range EXFILE must be purchased.

Note: When SET programming devices using E3.02, incorrect checksums are displayed. When verifying with checksums on, the correct checksums are displayed. This also occurs when programming EEPROMS from MASTER in any gang or single.

This is corrected in E3.03.
Please order new software if required.

UNIVERSE

New MCU 4.02

This allows the following:-

1. solves XON XOFF remote control bug
2. allows configuration of Universe to always power up in remote mode. (if you require this please state at time of ordering).

New L Stack Software L3.33

This allows the following:-

1. complete selection for 4 National PALs (see device listing)
2. security fuse programming implemented on Cypress C PALs.

Note: Samsung C PAL's can be programmed on the Cypress selection (see device listing).

Ian Toplis
Technical Support

ADDITIONS TO THE QUARTERLY ELAN DEVICE LISTINGS "PROGRAMABILITY"

CURRENT ISSUE DATED 20-4-89, (last issue 9-12-88).

ADDITIONS to 20-4-89

EPROM LIST : Added SGS 27C64 and 27C1024,
National Semiconductor 27C010,
Intel 28F512, 28F010,
XICOR 28C64D. (64 Byte Page write)

PAL LIST : Added National 16L8B, 16R4B, 16R6B, 16R8B selections.
Plus Samsung 16L8, 16R4, 16R8, 16L4.

MANUAL & FIRMWARE REVISION LEVELS. 03-7-89

CURRENT FIRMWARE REVISION LEVELS:

NEW 3/4/5000 SERIES	E 3.03 (5000 Micro Zifpac U1.01)
MASTER CONTROL UNIT (MCU)	M4.02
1015/H-STACK	H3.0
1013/G-STACK	G3.33 Type 7 for hardware level HW3.0 inclusive onward, Type 4 for earlier hardware issues.
1012/P-STACK	P3.30
1011/L-STACK	L3.33
1014/F-STACK	F3.04
EXFILE	2.01
ELAN LOGIC	3.2
EASYCOM	3.04
E8B	EB10A
C41	C13A (C14A WHEN E13 ADAPTER REQUIRED)
E12C	ESB20A

CURRENT MANUAL REVISION LEVELS

NEW SERIES; 3000 1.4, 4000 SERIES 1.2, 5000 SERIES 1.23

EB SERIES	1.92
C41	1.84
MCU	2.1
L-STACK 1011	2.0
P-STACK 1012	2.0
G-STACK 1013	2.1
F-STACK 1014	1.2
H-STACK 1015	0.1
EASYCOM	1.1
E12C	1.1
E9C	1.2

® Intel Corporation (U.K.) Ltd.
Pipers Way, Swindon SN3 1RJ
Telephone: (0793) 696000 Telex: 444447/8

PROGRAMMER QUALIFICATION APPROVAL

ELAN

Is hereby approved as meeting the programming specifications for the following components:

Device	Model	Module	Revision	Qual Date
2764A	Series 5000	ZIFPAC -142	E2.10-HW 2.00	8th June 1989
P2764A	Series 5000	ZIFPAC -142	E2.10-HW 2.00	8th June 1989
27C64	Series 5000	ZIFPAC -142	E2.10-HW 2.00	8th June 1989
27128A	Series 5000	ZIFPAC -142	E2.10-HW 2.00	8th June 1989
P27128A	Series 5000	ZIFPAC -142	E2.10-HW 2.00	8th June 1989
27C128	Series 5000	ZIFPAC -142	E2.10-HW 2.00	8th June 1989
27256	Series 5000	ZIFPAC -142	E2.10-HW 2.00	8th June 1989
P27256	Series 5000	ZIFPAC -142	E2.10-HW 2.00	8th June 1989
27C256	Series 5000	ZIFPAC -142	E2.10-HW 2.00	8th June 1989
27512	Series 5000	ZIFPAC -142	E2.10-HW 2.00	8th June 1989
27513	Series 5000	ZIFPAC -142	E2.10-HW 2.00	8th June 1989
27010	Series 5000	ZIFPAC -142	E2.10-HW 2.00	8th June 1989
27C010	Series 5000	ZIFPAC -142	E2.10-HW 2.00	8th June 1989
27011	Series 5000	ZIFPAC -142	E2.10-HW 2.00	8th June 1989
27210	Series 5000	ZIFPAC -142	E2.10-HW 2.00	8th June 1989
27F64	Series 5000	ZIFPAC -142	E2.10-HW 2.00	8th June 1989
27F256	Series 5000	ZIFPAC -142	E2.10-HW 2.00	8th June 1989
28F256P1	Series 5000	ZIFPAC -142	E2.10-HW 2.00	8th June 1989
28F256P2	Series 5000	ZIFPAC -142	E2.10-HW 2.00	8th June 1989

This qualification is for programmers constructed with the same design and components as the one tested. You are requested to advise us if a change is made in design or materials for this programmer model. We will then retest the revised product.

Approved by:

Gary Fisher

Gary Fisher

Ray Delissio

MEMORY COMPONENTS PRODUCT ENGINEERING

Lattice Semiconductor Corporation™

Date: 6/16/1989

Attention: Ian Toplis

Company: Elan Digital Systems

Fax Number: 011-44-293-518-591

From : Jim Larsen

Copy: Pat Crain

Company: Elan US

Fax: (415) 932-1722

Number of pages : 1
(Including this page)

If you do not receive all of these pages, Please call us at (503) 681-0118 as soon as possible. Lattice FAX number: (503) 681-0347.

Dear Mr. Toplis,

Lattice just finished reviewing the elan Model 1000 programmer and I am happy to inform you that you passed qualification for the GAL16V8 and GAL20V8 devices. The 16V8 and 20V8 algorithms will need some minor modifications in the future. I suggest doing them when your implementing the GAL16V8A and GAL20V8A algorithms.

I will be sending the Review Reports for your records this week. If there is anything I can do please let me know. Thank you very much for all of your efforts.

Best regards,

Jim Larsen
Hardware Development Engineer
Lattice Semicondcutor