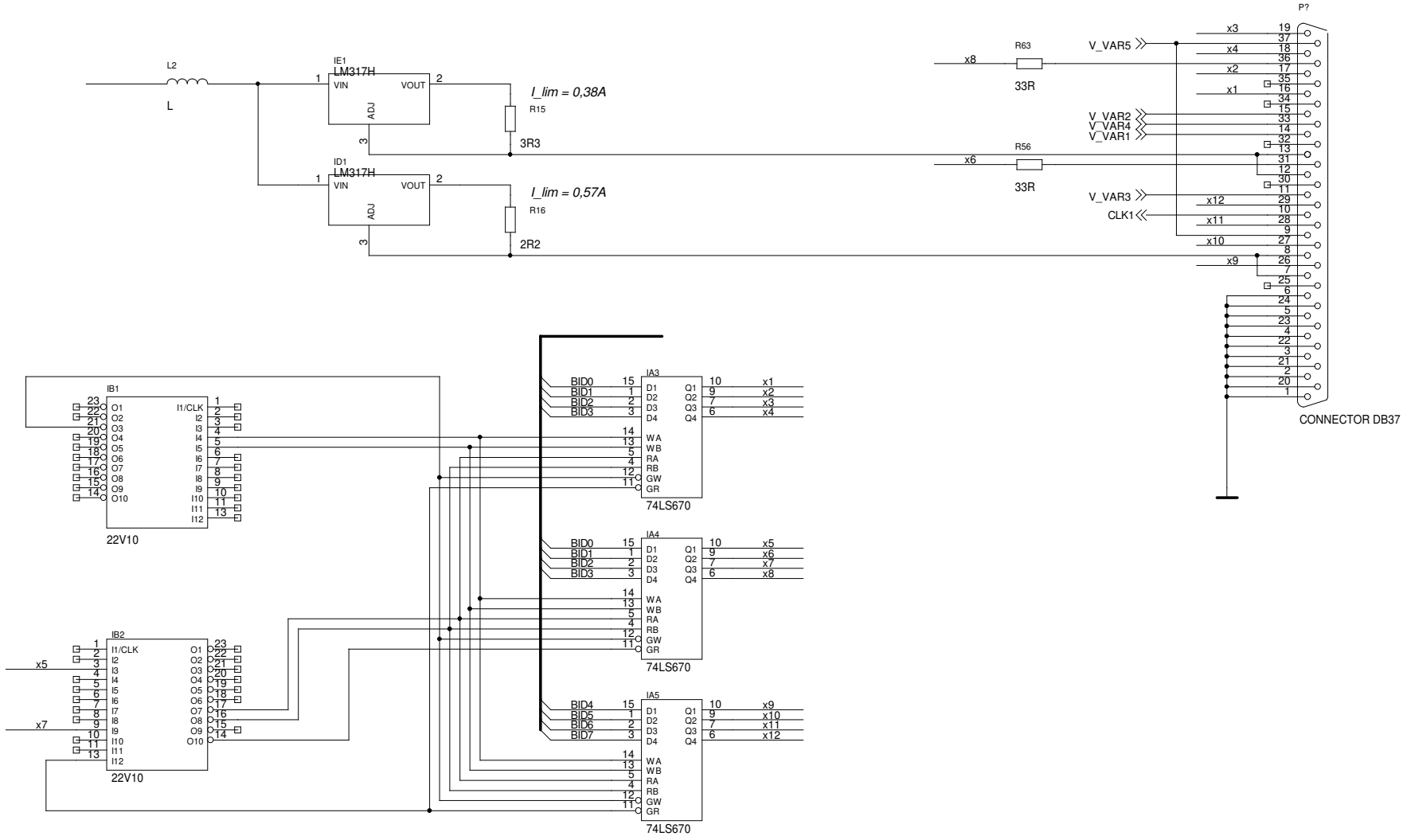
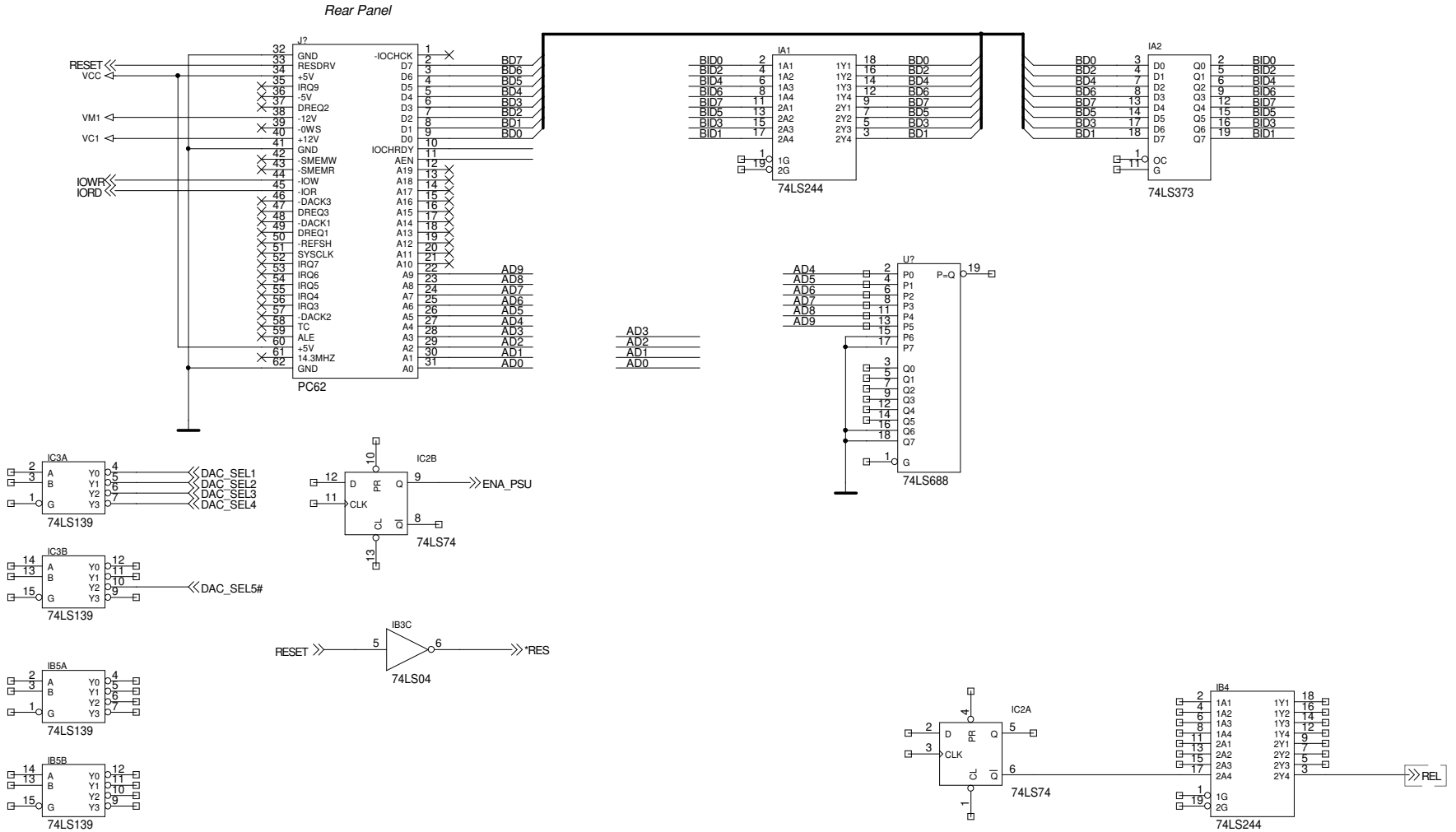
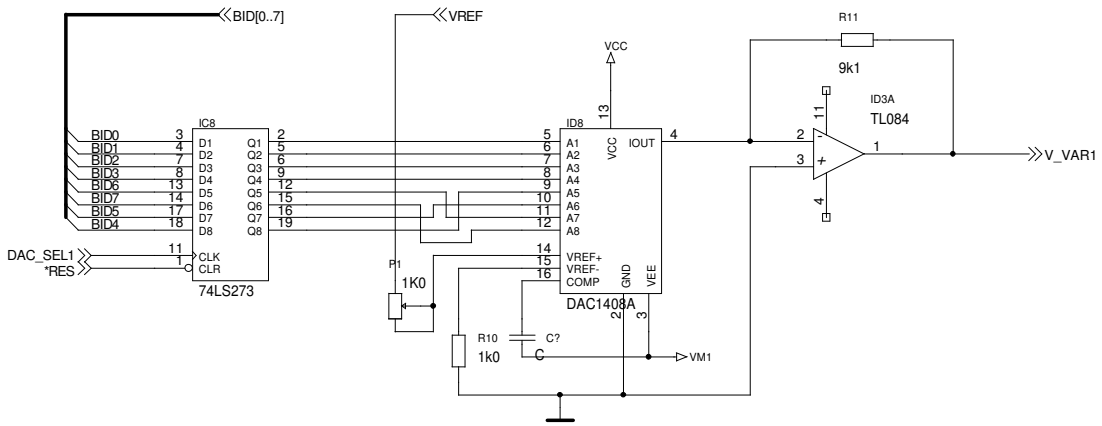
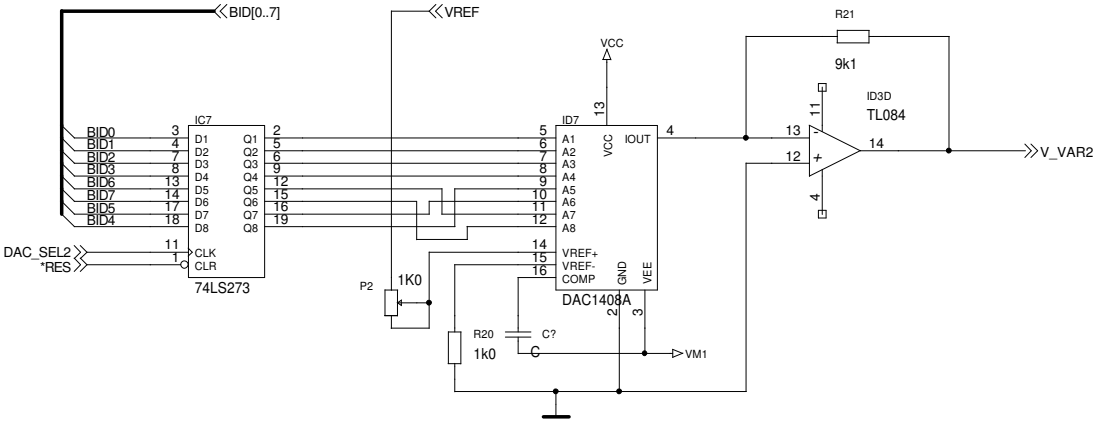
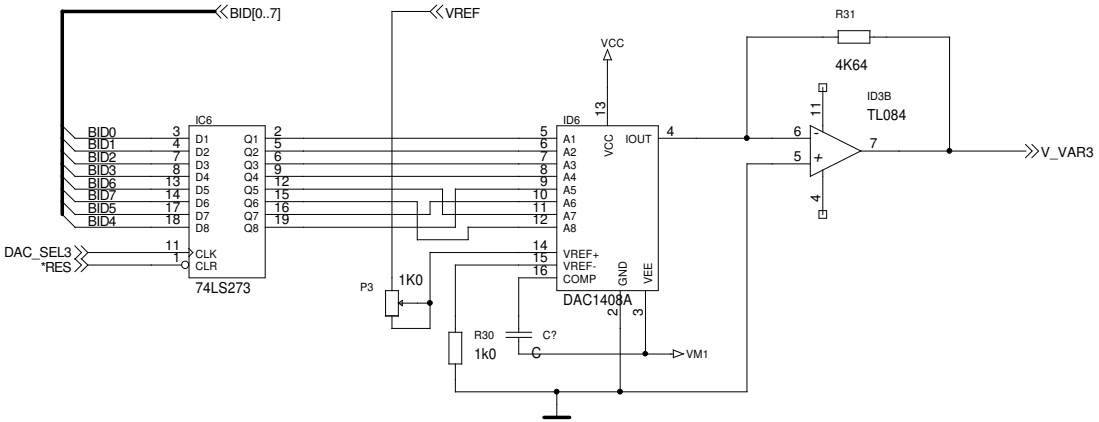
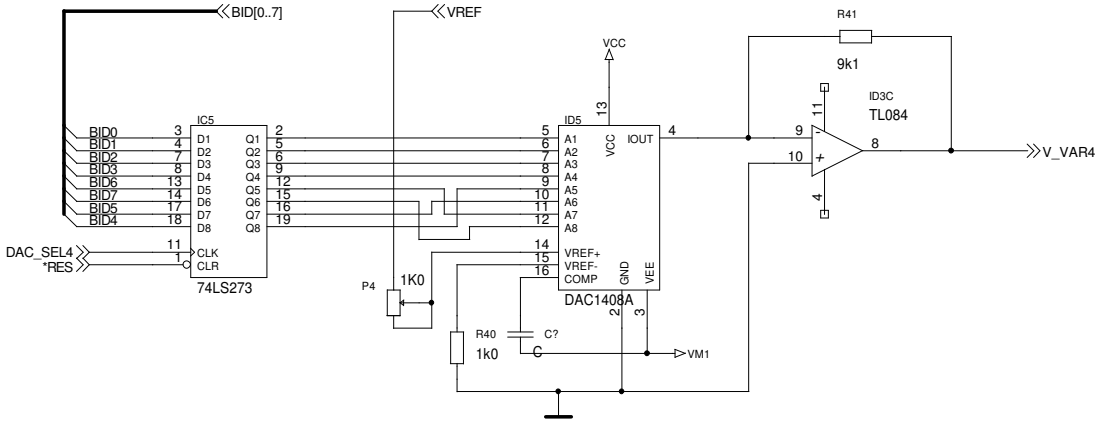

L2
L
IE1
LM317H
VIN
VOUT
ADJ
I_lim = 0,38A
R15
3R3
ID1
LM317H
I_lim = 0,57A
R16
2R2
R63
33R
R56
33R
x8
x6
V_VAR5
V_VAR2
V_VAR4
V_VAR1
V_VAR3
CLK1
P?
CONNECTOR DB37
IB1
22V10
IB2
22V10
IA3
IA4
IA5
74LS670
BID0
BID1
BID2
BID3
BID4
BID5
BID6
BID7
WA
WB
RA
RB
GW
GR
x1
x2
x3
x4
x5
x6
x7
x8
x9
x10
x11
x12

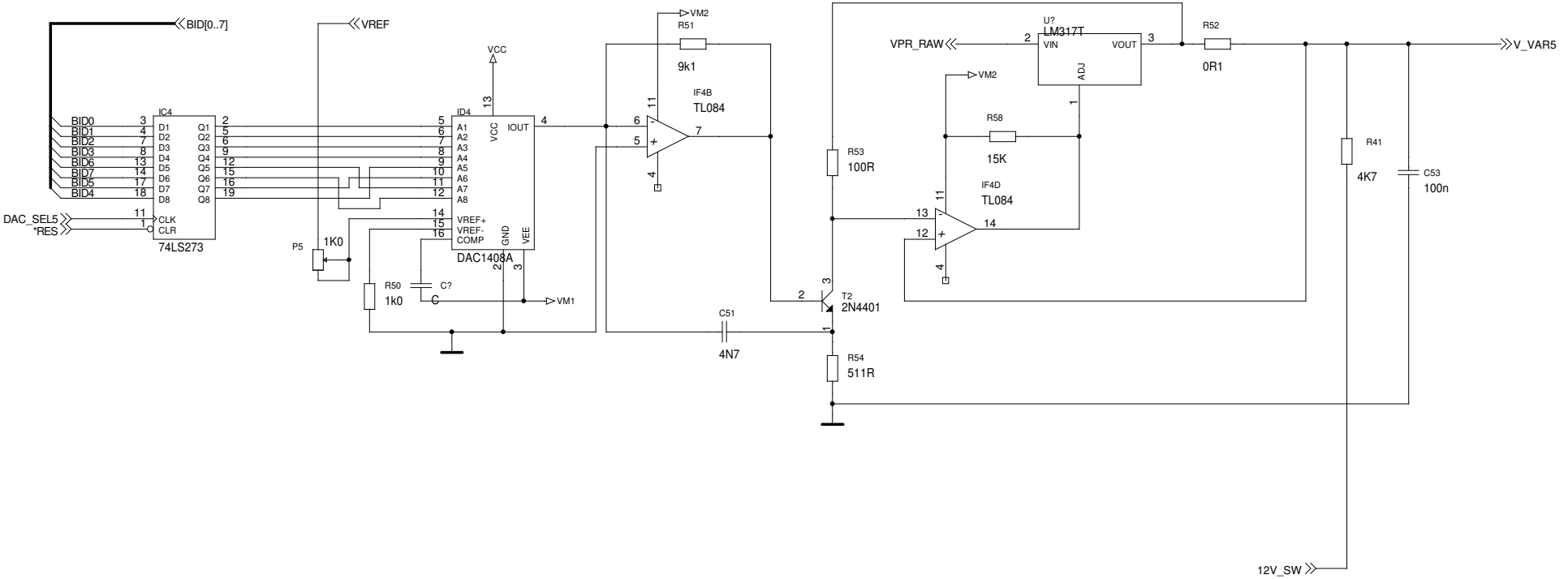

Rear Panel
J?
PC62
RESET
VCC
VM1
VC1
IOWR
IORD
GND
RESDRV
+5V
IRQ9
-5V
DREQ2
-12V
-0WS
+12V
GND
-SMEMW
-SMEMR
-IOW
-IOR
-DACK3
DREQ3
-DACK1
DREQ1
-REFSH
SYSCLK
IRQ7
IRQ6
IRQ5
IRQ4
IRQ3
-DACK2
TC
ALE
+5V
14.3MHZ
GND
-IOCHCK
D7
D6
D5
D4
D3
D2
D1
D0
IOCHRDY
AEN
A19
A18
A17
A16
A15
A14
A13
A12
A11
A10
A9
A8
A7
A6
A5
A4
A3
A2
A1
A0
BD7
BD6
BD5
BD4
BD3
BD2
BD1
BD0
AD9
AD8
AD7
AD6
AD5
AD4
AD3
AD2
AD1
AD0
IA1
74LS244
IA2
74LS373
U?
74LS688
P=Q
P0
P1
P2
P3
P4
P5
P6
P7
Q0
Q1
Q2
Q3
Q4
Q5
Q6
Q7
G
BID0
BID2
BID4
BID6
BID7
BID5
BID3
BID1
1A1
1A2
1A3
1A4
2A1
2A2
2A3
2A4
1Y1
1Y2
1Y3
1Y4
2Y1
2Y2
2Y3
2Y4
1G
2G
D0
D1
D2
D3
D4
D5
D6
D7
OC
IC3A
74LS139
DAC_SEL1
DAC_SEL2
DAC_SEL3
DAC_SEL4
IC3B
DAC_SEL5#
IB5A
IB5B
IC2B
74LS74
ENA_PSU
IB3C
74LS04
RESET
*RES
IC2A
REL
IB4

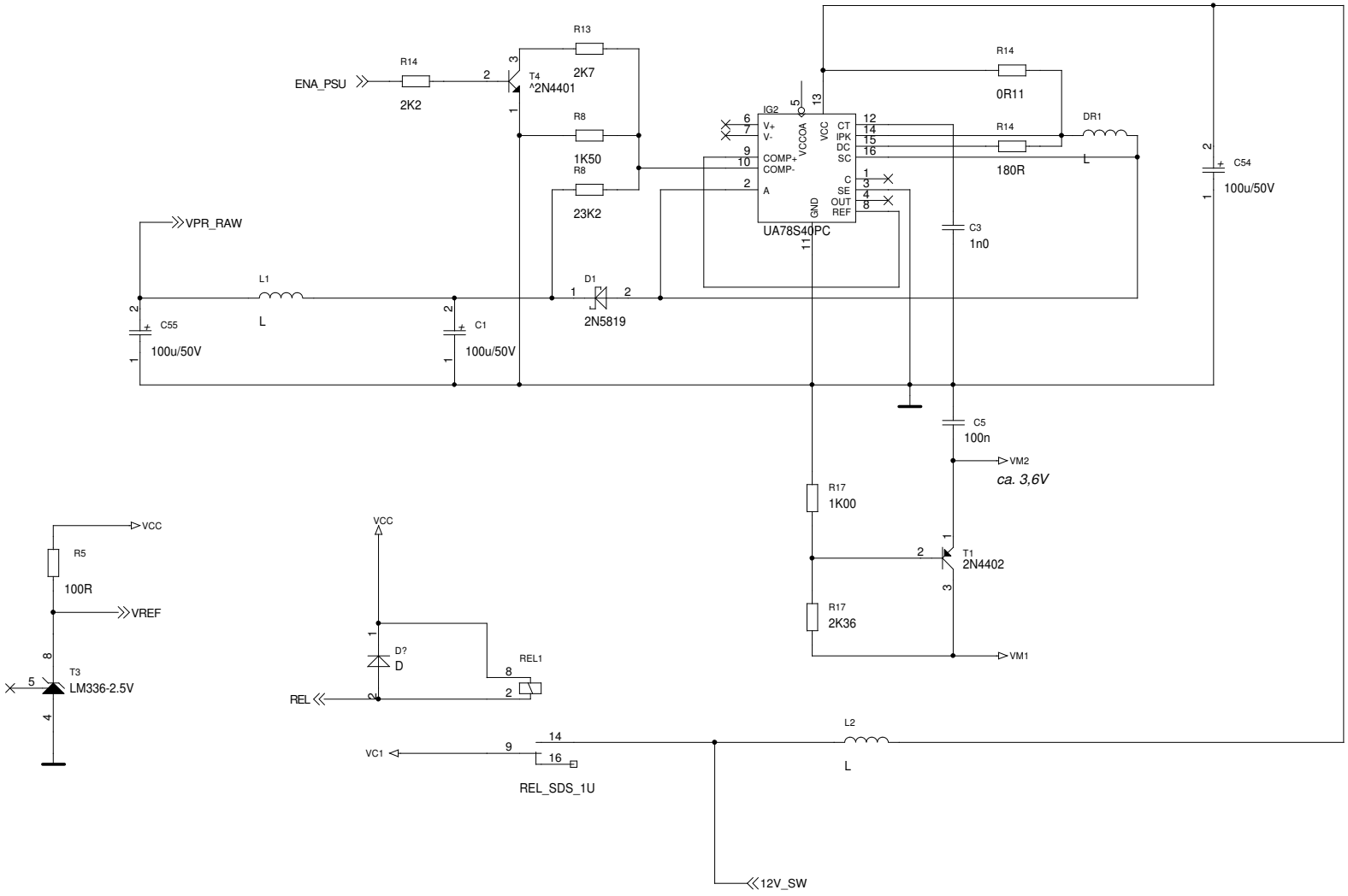

BID[0..7]
VREF
VCC
R11
9k1
ID3A
TL084
IC8
ID8
BID0
BID1
BID2
BID3
BID6
BID7
BID5
BID4
D1
D2
D3
D4
D5
D6
D7
D8
Q1
Q2
Q3
Q4
Q5
Q6
Q7
Q8
CLK
CLR
74LS273
DAC_SEL1
*RES
A1
A2
A3
A4
A5
A6
A7
A8
IOUT
VREF+
VREF-
COMP
GND
VEE
DAC1408A
V_VAR1
P1
1K0
R10
1k0
C?
C
VM1

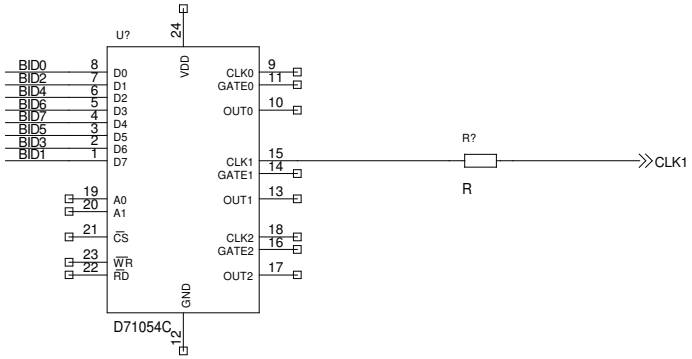

BID[0..7]
VREF
VCC
R21
9k1
IC7
74LS273
BID0
BID1
BID2
BID3
BID6
BID7
BID5
BID4
D1
D2
D3
D4
D5
D6
D7
D8
Q1
Q2
Q3
Q4
Q5
Q6
Q7
Q8
DAC_SEL2
*RES
CLK
CLR
ID7
DAC1408A
A1
A2
A3
A4
A5
A6
A7
A8
VCC
IOUT
VREF+
VREF-
COMP
GND
VEE
ID3D
TL084
V_VAR2
P2
1K0
R20
1k0
C?
C
VM1


BID[0..7]
VREF
VCC
R31
4K64
ID3B
TL084
IC6
ID6
BID0
BID1
BID2
BID3
BID6
BID7
BID5
BID4
D1
D2
D3
D4
D5
D6
D7
D8
Q1
Q2
Q3
Q4
Q5
Q6
Q7
Q8
A1
A2
A3
A4
A5
A6
A7
A8
IOUT
VCC
GND
VEE
VREF+
VREF-
COMP
CLK
CLR
DAC_SEL3
*RES
74LS273
DAC1408A
P3
1K0
R30
1k0
C?
C
VM1
V_VAR3


BID[0..7]
VREF
VCC
R41
9k1
IC5
ID5
ID3C
TL084
BID0
BID1
BID2
BID3
BID6
BID7
BID5
BID4
D1
D2
D3
D4
D5
D6
D7
D8
Q1
Q2
Q3
Q4
Q5
Q6
Q7
Q8
A1
A2
A3
A4
A5
A6
A7
A8
IOUT
V_VAR4
DAC_SEL4
*RES
CLK
CLR
74LS273
P4
1K0
VREF+
VREF-
COMP
GND
VEE
DAC1408A
R40
1k0
C?
C
VM1


BID[0..7]
VREF
VCC
VM2
R51
9k1
IF4B
TL084
U?
LM317T
VIN
VOUT
ADJ
VPR_RAW
R52
0R1
V_VAR5
IC4
BID0
BID1
BID2
BID3
BID6
BID7
BID5
BID4
D1
D2
D3
D4
D5
D6
D7
D8
Q1
Q2
Q3
Q4
Q5
Q6
Q7
Q8
DAC_SEL5
*RES
CLK
CLR
74LS273
ID4
A1
A2
A3
A4
A5
A6
A7
A8
IOUT
VREF+
VREF-
COMP
GND
VEE
DAC1408A
P5
1K0
R50
1k0
C?
C
VM1
R53
100R
R58
15K
IF4D
TL084
R41
4K7
C53
100n
T2
2N4401
C51
4N7
R54
511R
12V_SW


ENA_PSU
R14
2K2
T4
^2N4401
R13
2K7
R8
1K50
R8
23K2
VPR_RAW
L1
L
C55
100u/50V
C1
100u/50V
D1
2N5819
IG2
V+
V-
VCCOA
VCC
COMP+
COMP-
A
GND
CT
IPK
DC
SC
C
SE
OUT
REF
UA78S40PC
R14
0R11
R14
180R
DR1
L
C54
100u/50V
C3
1n0
C5
100n
VM2
ca. 3,6V
R17
1K00
T1
2N4402
R17
2K36
VM1
VCC
R5
100R
VREF
T3
LM336-2.5V
VCC
D?
D
REL1
REL
VC1
REL_SDS_1U
L2
L
12V_SW


U?
24
VDD
BID0
BID2
BID4
BID6
BID7
BID5
BID3
BID1
8
7
6
5
4
3
2
1
D0
D1
D2
D3
D4
D5
D6
D7
19
20
A0
A1
21
CS
23
22
WR
RD
CLK0
GATE0
OUT0
CLK1
GATE1
OUT1
CLK2
GATE2
OUT2
9
11
10
15
14
13
18
16
17
GND
12
D71054C
R?
R
CLK1