Operating Manual for the Orbit 48 Portable Programmer

Stag Programmers Ltd.
Silver Court, Watchmead,
Welwyn Garden City,
Hertfordshire,
AL7 1LT, UK

Tel +44 1707 332148
Fax +44 1707 371 503
Email: sales@stag.co.uk
www.stag.co.uk

803 1145 Issue 2
CIS 3012

Contents

1. Introduction

The Orbit 48 is a portable programmer for EPROMs, Micros and CMOS PLDs which is extremely simple to use . In Local operation, all functions are accessed directly from the Keypad in conjunction with menus and prompts displayed on the in-built LCD.

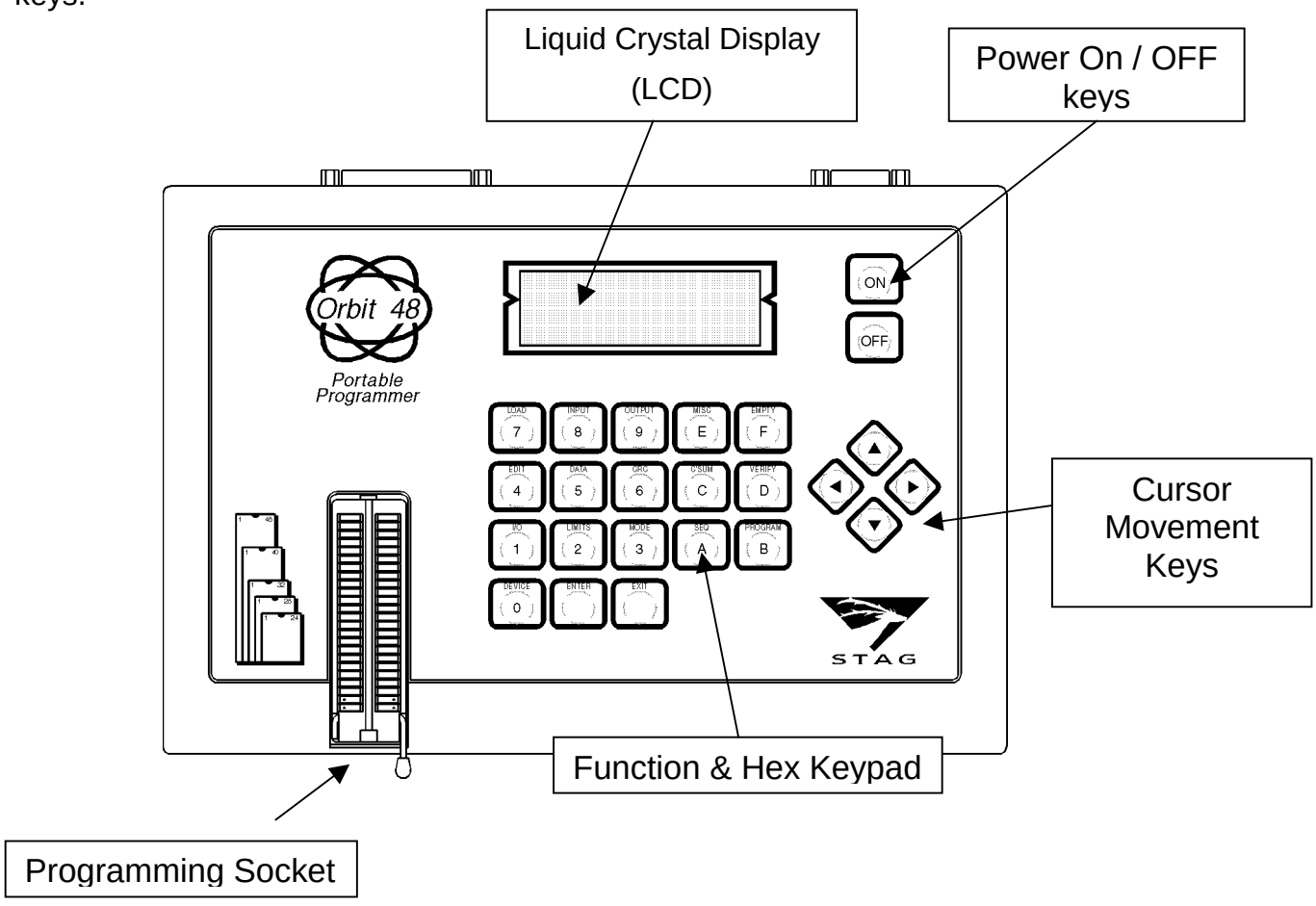
In Remote Mode using the optional software, Orbit 48 is controlled from either a Windows or DOS graphical environment.

Orbit 48 is powered from internal batteries which can be charged from the supplied mains unit.

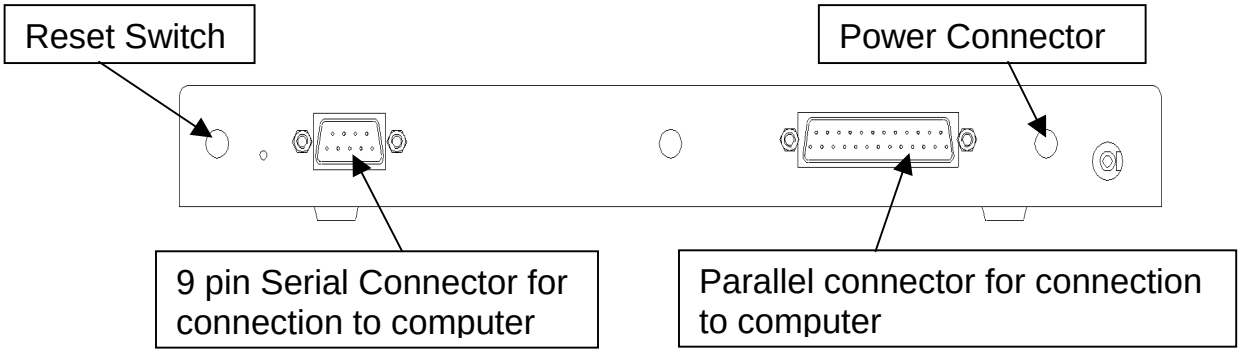
Before powering up the Orbit 48, ensure there is no device in the programming socket.

1.1 Control Panel (keypad & LCD)

The Control Panel is located on the top of the Orbit 48. It consists of an LCD to display status, errors, edit data, etc. and a full hexadecimal keypad, dedicated function keys and cursor keys.

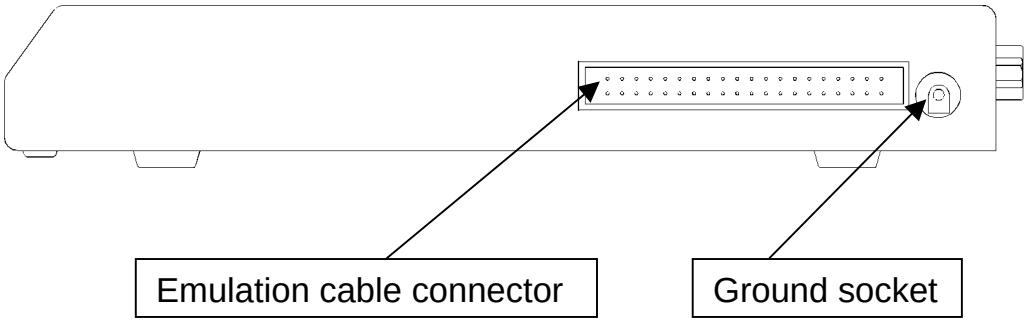


1.2 Rear Panel



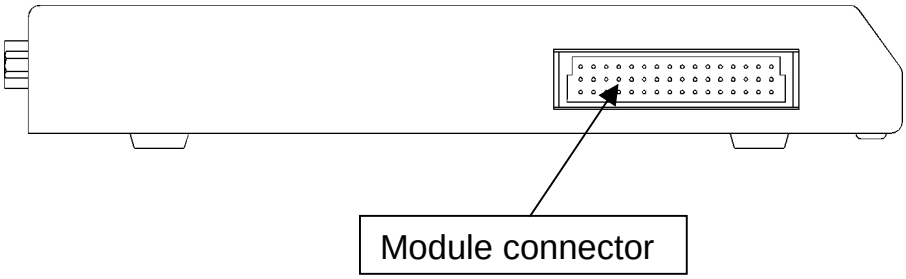
1.3 Right Side Panel

The mains power unit plugs into a socket on the right hand side of the Orbit 48.



1.4 Left Side Panel

Ensure Orbit 48 is powered down before plugging in a module. Use the retaining strap on the underside to ensure secure connection.



1.5 The keypad

ON	powers up Orbit 48
OFF	powers down Orbit 48 EXCEPT WHEN PERFORMING A DEVICE FUNCTION - THIS PROTECTS THE DEVICE
CRC	calculates a Cyclic Redundancy Check of data in RAM for PROMs & Micros or an Open Link Count for PLDs.
CSUM	calculates the checksum of data in RAM for PROMs and Micros or a JEDEC checksum for PLDs..
DATA	Performs additional manipulation functions of data in RAM and allows selection of emulation.
DEVICE	select a device by manufacturer and type.
EDIT	to manually edit data in RAM.
EMPTY	to perform an empty-check on a device.
ENTER	to accept a mode or function setting.
EXIT	to exit from a mode or function.
INPUT	to input data from the serial port into RAM.
I/O	to set all input/output parameters.
LIMITS	to over-ride the default limits for RAM and device data.
LOAD	to load data from a master device into RAM.
MISC	to perform miscellaneous additional functions and provide battery charge status information.
MODE	to set the bit-mode, e.g.: 8, 16 or 32.
OUTPUT	to output data from RAM to the serial port.
PROGRAM	to program data from RAM into a device.
SEQ	to set the programming sequence.
VERIFY	to compare data in RAM against data in a device.

The keys labelled 0-9, A-F are also used to enter numeric data when required.

- ↓ to scroll data up the screen.
- ↑ to scroll data down the screen.
- ← to move cursor left or display previous option.
- → to move cursor right or display next option.

2. General Operating Instructions

2.1 Local Operation

All functions are menu driven. Use the ↑ and ↓ keys to select the required option, then press ENTER.

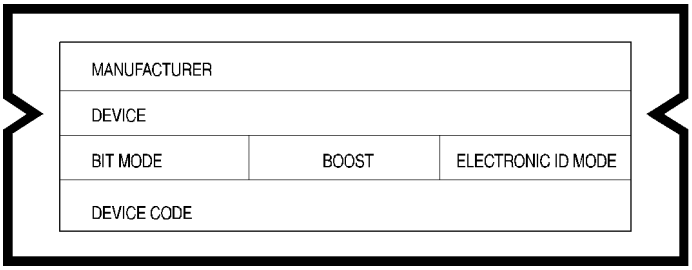
The option which will be selected is always the 2nd row on the display. This is indicated by the pointers to either side of the LCD

To abort from any menu:

☜ **EXIT**

The pointing finger symbol denotes a dedicated function key press.

The display consists of 6 message areas - shown in the diagram below.



The BOOST message is only displayed when the Orbit 48 is on boost charge, during which time, no device functions may be executed.

2.1.1 Device Selection

DEVICE

After DEVICE has been pressed:

the device code may be entered directly using 0-9, A-F. (The device codes are given in the Device Support List supplied with the Orbit 48 or any subsequent software upgrades).

To edit the code use the ← and → keys. When correct press **ENTER**.

OR

use the ↑ and ↓ keys to select the required manufacturer, then press **ENTER.**

Now use the ← and → keys to select the required family or size of device, then the ↑ and ↓ keys for the exact device, finally press **ENTER**.

See also Section 2.4.3 -Electronic identifier

Should a device requiring a module be selected when the relevant module is not fitted, an error message will be displayed detailing the module needed.

2.1.2 Device limits - applicable to PROMs and Micros only

All device functions (e.g. Load or Program) have 3 associated parameters:

DEV START the device address from which the function should start;

DEV STOP the device address at which the function should stop;

RAM START the RAM address from which the functions should start;

These are also used when calculating the check sum and CRC, and can be altered by the user.

LIMITS

Enter the addresses (in Hexadecimal) using 0-9, A-F.

The cursor can be moved using the ← and → keys. When correct press **ENTER**.

If invalid addresses are chosen (e.g DEV START higher than DEV STOP) the ENTER key will not let the user out of the function until valid addresses have been selected.

If the EXIT key is pressed the limits will not be changed from their previous values.

! The default limits for a device (corresponding to its size) will be used when a new device is selected.

2.1.3 Setting up the I/O

An I/O port can be used to input and output data from the Orbit 48's internal RAM.

See also: Section 3.6 Transferring Data via the ports
Section 5 Remote Control.

2.1.4 Selecting and Setting Up a Port

 **I/O**

then select **PORT**

A list of parameters is displayed.

These can be scrolled up and down using the ↑ and ↓ keys.

the option - displayed on the second line - may be changed using the ← and → keys.

When the whole menu is set-up as required press **ENTER**.

PORT: The user has the choice of using Serial or bi-directional parallel. If the parallel port is selected, the SPEED, PARITY and STOP BITS options have no effect; data will be 8 bits with no parity.

SPEED: The serial port may be sent to: 1200, 2400,4800, 9600, 19K2, 38K4 or 115.2K baud.

PARITY: Three options are available:
EVEN parity with 7 data bits;
ODD parity with 7 data bits;
NONE i.e. no parity with 8 data bits.

Note that for binary transmissions(e.g. STAG BINARY) NONE should be selected.

STOP BITS: The number of stop bits transmitted after each byte of data may be set 1 or 2.

2.1.5 Select Data Transfer Formats

This function enables the user to select the data format for input and output.

 **I/O**

then select **FORMAT**. A list of available I/O formats is displayed.

The list can be scrolled up and down using the ↑ and ↓ keys. Typically, you might have a choice between: STAG HEX, BINARY, STAG BINARY, ASCII HEX SPACE, INTEL 16 BIT, INTEL 32 BIT, MOTOROLA S-REC.

Select the required format using the ↑ and ↓ keys, then press **ENTER**.

2.1.6 Bleeper control

After each function the bleeper will sound to indicate pass or fail (2 bleeps for pass, 5 bleeps for fail). This function may be disabled or enabled.

 **I/O**

then select **BLEEP**

Select disabled or enabled using the ← and → keys followed by **ENTER**.

You can also have the bleeper sound for each key press. Select disabled or enabled using the ← and → keys followed by **ENTER**.

2.2 Entering Remote Control

To put the unit into remote control:

 **I/O**

then select **REMOTE CONTROL**

 **I/O**

To quit from remote back into local mode, power down the unit, then power up with the EXIT key pressed.

see also Section 5 - Remote Control.

2.3 Bit Mode - applicable to PROMS only

The user has the choice of bit modes.

If an 8-bit wide PROM is selected then you may choose between 8 BIT, 16 BIT and 32 BIT.

If a 16-bit wide PROM is selected then you may choose between 16 BIT and 32 BIT.

Note that if a PLD is selected then you will not have a choice of bit modes.

The bit mode is used in all device functions (e.g. Load or Program), and is also used when calculating the checksum and CRC.

MODE

then select required mode.

Note that if the device is 16 bits wide then two bytes of RAM are required to store each device word. This can be done either high byte first/low byte last (the default), or else low byte first/high byte last.

Having selected the bit mode as detailed subsequently you will then be asked to specify the byte order. To do this, use the ← and → keys to make the selection, then press ENTER when ready.

2.3.1 8 Bit Mode

In this mode, assuming no offset is used, each byte in RAM is programmed to a corresponding address in a single target device.

2.3.2 16-bit mode

Byte Wide Devices

In 16-bit mode the RAM data will be split into ODD and EVEN bytes.

When performing any device function (such as Load or Program) other than Empty Check, the Orbit 48 will ask the user which device is required.

Press 0 for the device corresponding to EVEN bytes and 1 for the device corresponding to ODD bytes.

Word Wide Device

It is necessary to set whether the even bytes map to D0 - D7 or D8 - D15 of the device, i.e. which way round the bytes are ordered in the device.

2.3.3 32-bit Mode

This is similar to 16-bit mode.

Byte Wide Devices

Requires the operator to specify 0, 1, 2 or 3 for the device to be operated on.

Word Wide Devices (Word wide being 16 bits wide).

Requires the operator to specify 0 or 1 for the device to be operated on.

2.4 Programming Sequence

This allows the user to define what functions are performed when a device operation is required.

SEQ

Sub-menus are selected using the ↑ and ↓ keys, then pressing **ENTER**.

2.4.1 Pre Program Checks

SEQ

then select **PRE-PROGRAM**

Before a device is programmed, the device can be automatically checked with either an empty check or an illegal bit check or neither.

The empty check tests each location of the device (within the specified limits) to determine whether or not it is empty.

The illegal bit check tests each location of the device (within the specified limits) to determine whether it has bits which are programmed and required to be empty by the RAM data.

select using the ↑ and ↓ keys, then press **ENTER**.

see also section 2.5 - Displaying information about failures.

2.4.2 Marginal Verify Testing

SEQ

then select **MARGINAL TESTING**

After programming, during illegal bit test, and when the VERIFY key is pressed, the device is verified with the RAM. This can either be done at the manufacturer's recommended Vcc voltages (Marginal verify disabled), or at 4.5V and 5.5V (Marginal verify enabled). Note that the manufacturer's requirements may be more stringent than the ± 10% of the marginal verify.

Note: Marginal testing also applies to empty testing and illegal bit testing.

Select the required option using ↑ and ↓, then press **ENTER**.

See also Section 2.5 - Displaying information about failures.

2.4.3 Electronic Identifier

 **SEQ**

then select **ELECTRONIC ID**

An Electronic Identifier exists in most EPROM and EEPROM devices. It can be used to check or select a device before load/verify/empty check or program.

Three options are given: check, automatic, none.

NONE will check the electronic identifier in any way.

CHECK will check that the device in the socket is the same as that selected. If not, the error message WRONG PART will be displayed if no signature can be read from the device.

AUTOMATIC will read the identifier and try to select the correct device code to match. It can only select devices of the same family as that already selected. If a different device is inserted then the error message MISMATCHED PARTS will be displayed.

Select the required option using the ↑ and ↓ keys, then press **ENTER**.

2.4.4 Security Fuses

 **SEQ**

then select **SECURITY**

If the device has a security fuse or fuses to secure the data once programmed, the user can select to program them or leave them intact using the ← and → keys followed by **ENTER**. With devices that have more than one security fuse they can be selected using the ↑ and ↓ keys to display the other fuses, **ENTER** is then pressed once to enter all the fuses.

On some EEPROMs the security feature can be used to make the write protected.

! The security setting is reset to not secure when a new device is selected.

2.5 Displaying information about Failures

The display failures function must first be enabled if a failure log is to be displayed about a subsequent device function.

SEQ

then select **FAILURES**

then press ← or → to toggle the function on or off, then press **ENTER**.

If a device fails when the VERIFY key is pressed, the location and data of the failure can be displayed.

When enabled and a failure occurs, the following will be displayed:

VERIFYING

FAIL ADDR = aaaaaaa

RAM r l

DEV d l

where: aaaaaaaa is the address of the fail:

r l is the data in the RAM

d l is the data in the device;

All values are in hexadecimal.

The next fail is displayed by pressing ↓, or the function aborted by pressing **EXIT**.

! While the failures are being displayed the device is powered up and should not be removed from the socket.

2.6 Miscellaneous Set-ups and Functions

2.6.1 Machine's Statistics

 **MISC**

then select **STATISTICS**

This function will show the following information:

FLASH software revision

(the boot block's software revision is displayed on power up);

the RAM size (in bytes);

the FLASH size (in bytes);

2.6.2 Saving and Restoring the Machine's Set-up

The following information is stored automatically on power down:

The device - manufacturer and type;

all I/O selections;

the mode;

the programming sequence selections.

These settings are automatically restored on power up.

2.6.3 Battery Status

 **MISC**

then select **CHECK BATTERY**

This function will indicate the battery charge level.

2.6.3.1 Battery Charging and Management

Orbit 48 constantly monitors the charge state of its batteries. If the charge level becomes too low, Orbit will automatically shut down to preserve the integrity of its RAM after issuing the following message:

WARNING !!
Batteries Low
Powering Down

Recharging the batteries is achieved by plugging the supplied charger into the socket on the rear panel of the Orbit 48, and then connecting to the mains electricity supply. Boost charge mode will then be entered automatically, indicated in the Orbit's display. This will continue until the batteries are fully charged. The Orbit then switches to trickle charge mode.

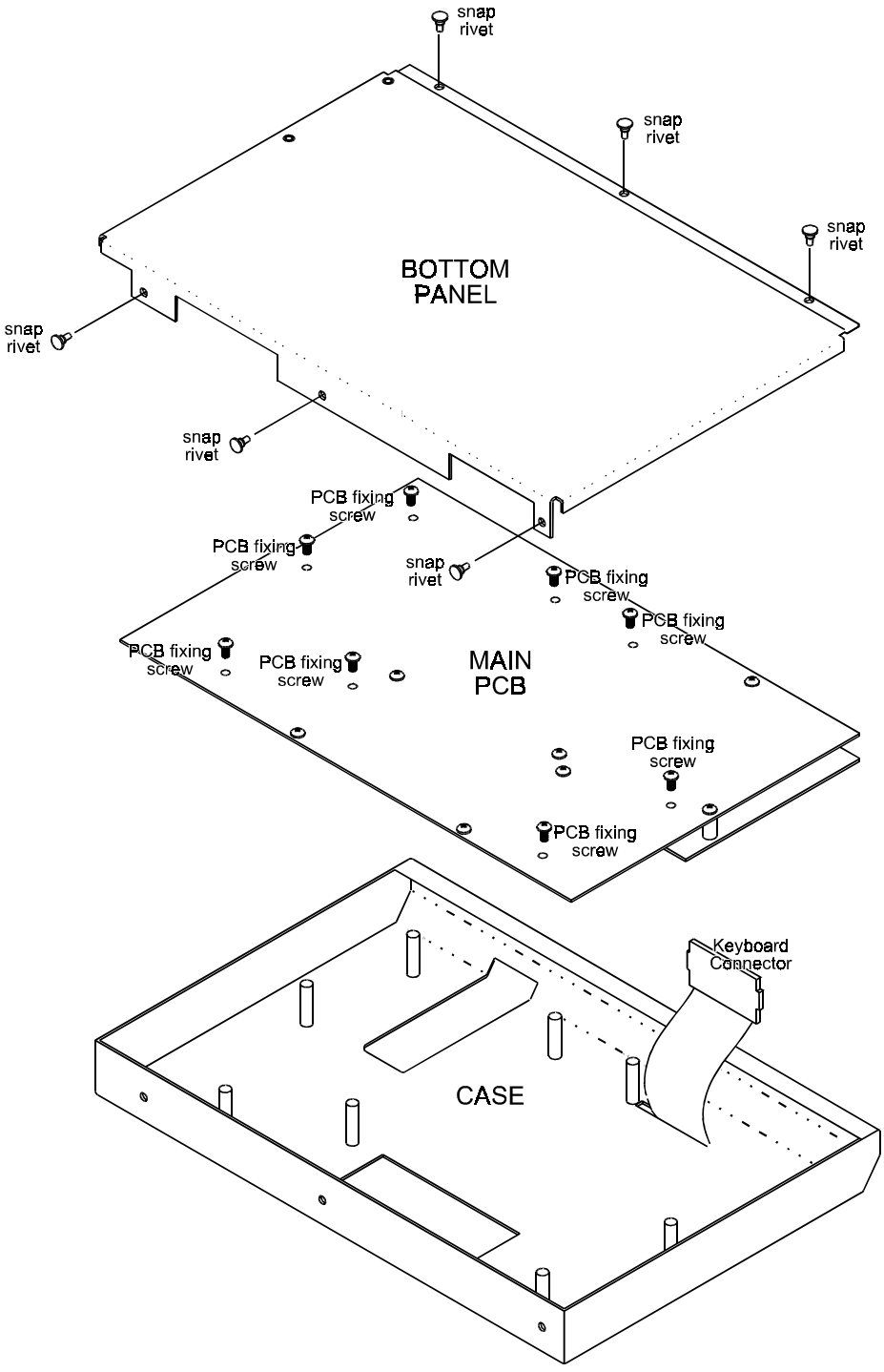
Device operations such as PROGRAM, LOAD, etc, cannot be performed during Boost mode.

2.6.3.2 Battery Removal

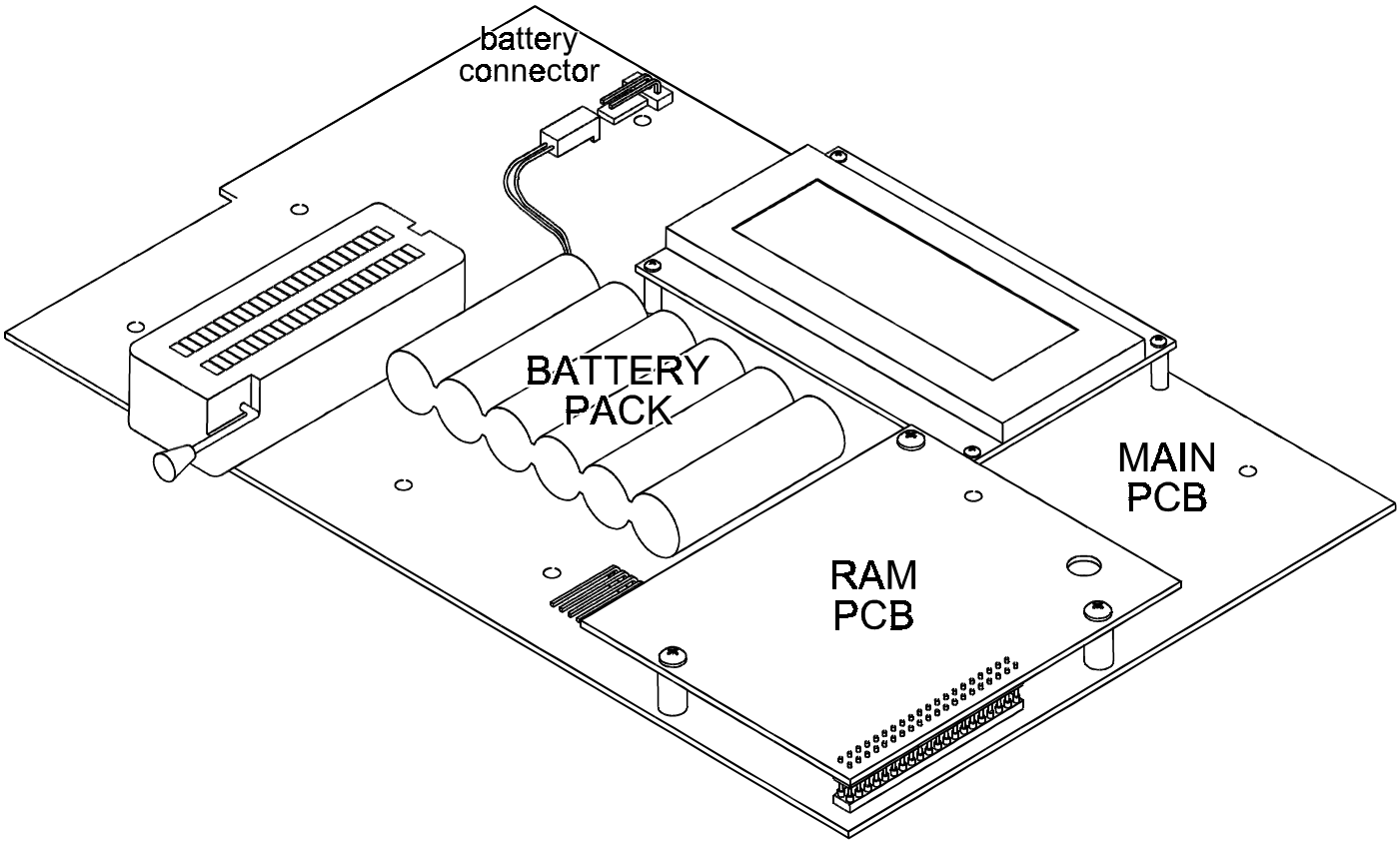
Should it become necessary to remove the battery pack, proceed as follows:

1 Ensure there is no device socketed
2 Ensure that data in RAM is not required
3 Power down unit and disconnect power unit, comms Cables, etc
4 Invert unit and place face down on clean, smooth surface
5 Lever out the 6 snap rivets - 3 along the front edge and 3 on the rear panel
6 Remove base panel to reveal underside of main PCB
7 Unscrew the 8 retaining screws - see diagram. Lift PCB away from case.
8 Disconnect keyboard ribboncable connector. This will fully release PCB.
9 Turn PCB the right way up. This will reveal the battery pack which can be disconnected at the polarised connector.
10 Reassembly is the reverse procedure to dissassembly.

diagram of disassembly of Orbit 48.



Removal of battery pack



2.6.4 Updating the Software

To update the software, first load the software from STAG into the unit's RAM. This can be done using either the I/O ports or by loading master devices.

MISC

then select **UPDATE**

Orbit 48 will then check that the data in the RAM has the correct format and CRC. If it has, the FLASH memory will now be updated. When complete the unit will re-start itself, as if just powered up.

2.6.5 Automatic Power Down If No Keypress

Selects the maximum time allowed between consecutive key presses before Orbit 48 automatically shuts down to conserve power.

 **MISC**

then select **KEY TIMEOUT**

A list of time out values is displayed:

NEVER

5 MIN

10 MIN

15 MIN

2O MIN

25 MIN

30 MIN

This list can be scrolled using the up and down cursor keys.

Select the required option and press **ENTER**.

2.7 RAM Expansion

To add expansion RAM, proceed as follows:

1 Ensure there is no device socketed

2 Ensure that data in RAM is not required

3 power down unit and disconnect power unit, comms cables etc

4 Invert unit and place face down on clean, smooth surface

5 Lever out the 6 snap rivet - 3 along the front edge and 3 on the rear panel

6 Remove base panel to reveal underside of main PCB

7 Unscrew the 8 retaining screws - see diagram. Lift PCB away from case

8 Disconnect keyboard ribbon cable at connector. This will fully release PCB.

9 Turn PCB the right way up. This will reveal the connector for the RAM expansion PCB

10 Plug in RAM expansion PCB and secure with screws.

11 Re-assembly is the reverse procedure to disassembly.

3. RAM Functions

3.1 Editing the RAM

This section details the functions which allow the user to alter data in the Orbit 48's RAM. You may not edit the RAM if a PLD is selected.

3.1.1 Listing and Changing the RAM

EDIT

The editor displays 4 addresses in the following format:

aaaaaaaa hh ddd c

Where:

aaaaaaaa	is the RAM address in hexadecimal;
hh	is the hexadecimal value stored at the location;
ddd	is the decimal value stored at the same location;
c	is the ASCII for that byte if printable (if not, a Character is displayed).

The address can be changed using 0-9, A-F and by moving the cursor using ← and →.
To edit the data move the cursor right to the hexadecimal or decimal data fields, then overwrite the data. To edit the next or previous byte use ↑ or ↓ .

When complete, press **ENTER** then **EXIT**.

Data can be listed by changing the address as above and then pressing **ENTER**, or by using the ↑ and ↓ to view the previous or next location. Press **EXIT** when finished.

3.2 RAM Data Manipulation

The following functions can be performed on the Orbit 48's internal RAM:

FILL RAM	BLOCK MOVE
INSERT BYTES	DELETE BYTES
COMPLEMENT RAM	STRING SEARCH

These functions apply only to PROMS and Micros. You may not manipulate RAM data if PLD is selected.

DATA

Select the function required using the ←and → keys, then press ENTER.

3.2.1 Fill the RAM

This function allows you to fill the RAM between selected limits with a selected bit pattern.

DATA

then select **FILL RAM**

On selecting 'FILL RAM' the following options are available:

Fill with Zeros (fill the RAM with 00 hex)

Fill with Ones (fill the RAM with FF hex)

Fill with Empty (fill the RAM with the empty state of the selected device)

Fill with Pattern (fill the RAM with a user defined pattern)

Select the option required using the ↑ and ↓ keys, then press **ENTER**.

If 'fill with pattern' is selected the desired pattern should be entered in hexadecimal using the keys 0-9 and A-F.

The ← and → keys may be used to move the cursor to edit the pattern. The ASCII value of the hexadecimal numbers is displayed underneath (if a printable value is entered).

When correct press **ENTER**.

! Note that patterns are only considered legal if they are 2, 4 or 8 hexadecimal characters long - according to selected bit mode.

All the options will then ask for the address range over which the fill is to take place.

The options are as follows.

ENTIRE MEMORY: This function fills the entire ram with the specified pattern.

DEVICE LIMITS: This will only fill the RAM used for the selected part, taking account of the selected device limits (see Section 2.1.2) and mode (see Section 2.3). Note that if the device address limits are set to only partially cover the device then this function may actually fill non-contiguous regions of RAM.

ARBITRARY LIMITS: This function will enable the user to fill RAM between entirely arbitrary RAM limits. On selecting this option the address limits should be entered in hexadecimal using 0-9, A-F,↑↓ and ← → to move the cursor as required.

Select the option required using the ↑ and ↓ keys, then press **ENTER**.

3.2.2 Move a Block of Data

 **DATA**

then select **BLOCK MOVE**

This function allows data to be moved from one section of RAM to another.

There are no restrictions on the positioning of either the source block or the destination block, other than that they must both fit within the physical available RAM. Source and destination blocks may even overlap, should this be required.

On selecting ' BLOCK MOVE' the RAM address of BLOCK START, BLOCK END and DESTINATION should be entered in hexadecimal using 0-9, ↑ ↓ and ← → to move the cursor as required. When correct press **ENTER.**

3.2.3 Inserting Bytes into RAM

 **DATA**

then select **INSERT BYTES**

This function allows a pattern of bytes to be inserted into RAM at a specific location.

All data at or beyond (i.e. at higher addresses than) the insertion will be moved upward in memory by the number or bytes inserted. No data bytes are overwritten at the insertion position - instead they move up to make room for the new data.

! As a result of this operation, the very last byte(s) in memory will be lost.

First enter the address in RAM to insert the first byte, use 0-9, A-F, ↑ ↓, ← → to move the cursor. When correct press **ENTER**.

Then the desired pattern should be entered in hexadecimal using 0-9, A-F.

The ← and → keys may be used to move the cursor to edit the pattern. The ASCII value of the hexadecimal numbers is displayed underneath (if printable value is entered). Up to 32 characters may be entered.

When correct press **ENTER**.

3.2.4 Deleting Bytes from RAM

DATA

then select **DELETE BYTES**

This function allows a number of bytes to be deleted from RAM. All data at or beyond (i.e. at higher addresses than) the deletion address will be moved down in memory by the number of bytes specified.

Enter the address in RAM to delete the first byte and the number of bytes to be deleted (in hexadecimal), use 0-9, A-F, and ↑ ↓ ← → to move the cursor. When correct press **ENTER**.

3.2.5 Complementing the RAM

DATA

then select **COMPLEMENT RAM**

This function allows the data in RAM to be complemented between selected limits. This means that every binary 1 in the RAM data is changed to a binary 0, and vice versa.

The address range over which the complement is to take place should then be selected.

The options are:

ARBITRARY LIMITS

ENTIRE MEMORY

DEVICE LIMITS

Select the option required using the and keys, then press **ENTER**.

The three functions available are as follows:

ENTIRE MEMORY: This function complements the entire RAM.

DEVICE LIMITS: This will only complement the RAM used for the selected part, taking account of the selected device limits (see Section 2.1.2) and mode (see Section 2.3). Note that if the device address limits are set to only partially cover the device then this function may actually complement non-contiguous regions of RAM.

ARBITRARY LIMITS: This function will enable the user to complement RAM between entirely arbitrary RAM limits. On selecting this option the address limits should be entered in hexadecimal using 0-9, A-F and ↑ ↓ ← → to move the cursor as required.

When correct press **ENTER**.

3.2.6 Search the RAM for a Data Sequence (STRING SEARCH)

This function allows you to search for a string of bytes within specified RAM limits.

 **DATA**

then select **STRING SEARCH**

This desired pattern should be entered in hexadecimal using 0-9, A-F. The ↑ ↓ ← → keys may be used to move the cursor to edit the pattern. The ASCII values of the hexadecimal numbers are displayed underneath (if printable values are entered).

Up to 32 characters may be entered.

When correct press **ENTER**.

The address range over which the search is to take place should then be selected.

The options are:

ARBITRARY LIMITS

ENTIRE MEMORY

DEVICE LIMITS

Select the option required using the ↑ ↓ keys, then press **ENTER**.

The three functions available are as follows:

ENTIRE MEMORY:	This function searches the entire RAM.
DEVICE LIMITS:	This will only search the area of RAM used for the selected device, taking account of the selected device limits (see Section 2.1.2) and mode (see Section 2.3).
ARBITRARY LIMITS:	This function will enable the user to search RAM between entirely arbitrary RAM limits. On selecting this option the address limits should be entered in hexadecimal using 0-9, A-F and ↑ ↓ ← → keys to move the cursor as required.

When correct press **ENTER**.

If the string search is successful then the address of the first byte of the string will be displayed.

Press **ENTER** to search for the next occurrence of the string, or **EXIT** to return to the top level. If no further occurrences are found ‘String not found’ will be displayed, pressing any key will return you to the top level displayed.

3.3 EMULATION

Emulation mode allows Orbit 48 to emulate a variety of 27xxx type devices up to 4Mbit in size (8 Mbit with RAM expansion).

 **DATA**

then select **EMULATION**

Once enabled, the Orbit continues to run normally but the target system has priority access to Orbit's RAM.

3.3.1 Emulation Procedure

Ensure that the emulation cable is configured correctly for the device to be emulated.

Connect the cable to the emulation port on the side of the Orbit 48.

Plug the cable into the target system. If the socket on the target system is for 24 or 28-pin device then plug in a turned pin socket of the appropriate size first.

This will ensure that the unused pins on the cable do not short to anything on the target system.

Load the emulation data into the bottom of the Orbit's RAM.

Enter emulation mode via the DATA menu.

To modify the data use the Orbit's editor in the usual way. This feature may not work with all target systems. Systems that tie OE or CE low will prevent editing all together and systems that run very fast may make editing unreliable.

To down load a completely new set of data, emulation must be disabled before the download begins.

If a function that is not compatible when emulation is attempted, the message:

ERROR

EMULATING

will be displayed

3.3.2 RAM Emulation

By using the write line from the emulation cable it is possible to emulate RAM. If emulating RAM it is important that the control signals are correctly implemented to avoid contention on the data bus. This means that the OE and WR lines should not be low at the same time.

3.3.3 Configuring the 32-pin Emulation Pod

The emulator cable may be used to emulate devices from the 2732 up to the

27040. Smaller devices may be emulated provided certain conditions are met. A 27080 can be emulated if the optional RAM expansion is fitted.

All unused address lines must be pulled low including pins that would correspond to VPP or PGM on a small device. This is achieved simply for devices in the range 2732 up by switching the appropriate switches, marked A12 to A19, to the off position.

e.g. For a 27128 A12 & A13 would be on & all other address switches would be off.

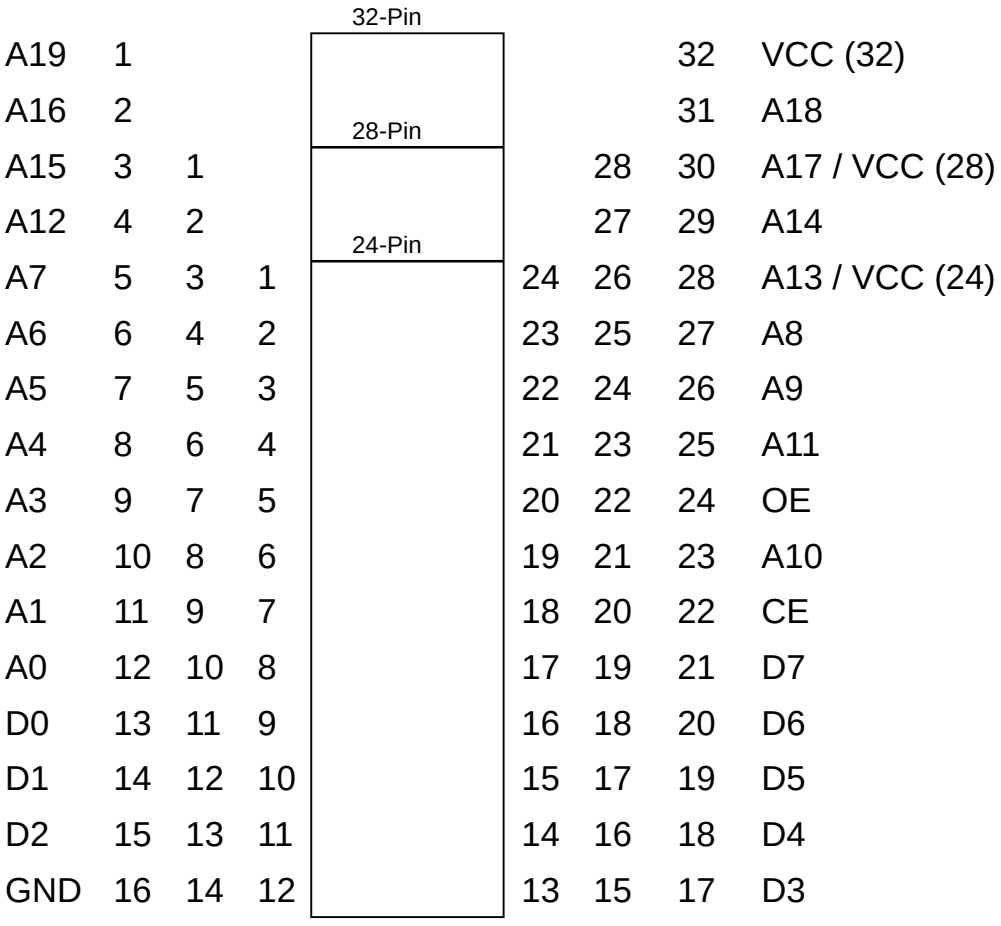
All Vcc switches should be off except for the one corresponding to the size of the selected device. The related address line should be turned off.

e.g. For a 2764 VCC(28) would be on and A17 would be off.

For devices smaller than a 2732 address, switches aren't available so care should be taken to ensure that unused address lines are pulled low.

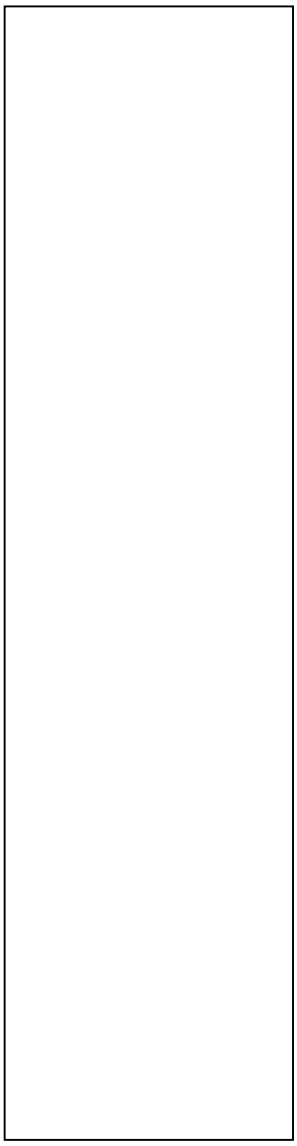
If the WRITE cable is not being used, the switch marked WRITE must be set to OFF

3.3.4 32-pin Emulation Pod Pinout



A19	1						32	VCC (32)
A16	2						31	A18
A15	3	1				28	30	A17 / VCC (28)
A12	4	2				27	29	A14
A7	5	3	1		24	26	28	A13 / VCC (24)
A6	6	4	2		23	25	27	A8
A5	7	5	3		22	24	26	A9
A4	8	6	4		21	23	25	A11
A3	9	7	5		20	22	24	OE
A2	10	8	6		19	21	23	A10
A1	11	9	7		18	20	22	CE
A0	12	10	8		17	19	21	D7
D0	13	11	9		16	18	20	D6
D1	14	12	10		15	17	19	D5
D2	15	13	11		14	16	18	D4
GND	16	14	12		13	15	17	D3

3.3.5 40 pin EPROM Emulation Pod Pinout



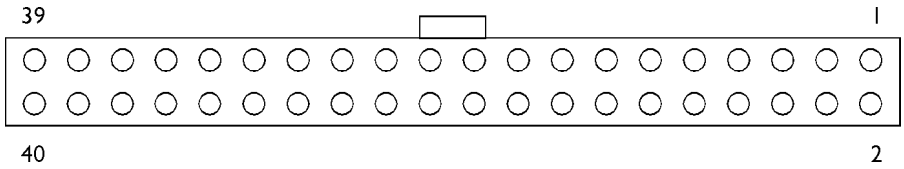
Vpp	1	40	Vcc
CE	2	39	A17
D15	3	38	A16
D14	4	37	A15
D13	5	36	A14
D12	6	35	A13
D11	7	34	A12
D10	8	33	A11
D9	9	32	A10
D8	10	31	A9
GND	11	30	GND
D7	12	29	A8
D6	13	28	A7
D5	14	27	A6
D4	15	26	A5
D3	16	25	A4
D2	17	24	A3
D1	18	23	A2
D0	19	22	A1
OE	20	21	A0

3.3.5.1 Configuring the 40-pin Pod

The 40-pin emulation pod may be used to emulate devices from a 271024 to a 274096.

Just as for the 32-pin pod, switches corresponding to unused address lines must be set to OFF.

3.3.6 Orbit Emulator Socket Pinout



Pin	Signal	Pin	Signal
1	GND	2	D3
3	D2	4	D4
5	D1	6	D5
7	D0	8	D6
9	A0	10	D7
11	A1	12	CE
13	A2	14	A10
15	A3	16	OE
17	A4	18	A11
19	A5	20	A9
21	A6	22	A8
23	A7	24	A13
25	A12	26	A14
27	A15	28	A17
29	A16	30	A18
31	A19	32	D8
33	D9	34	WR
35	D11	36	D10
37	D12	38	D13
39	D15	40	D14

3.4 Checksum of RAM Data

CSUM

For PROMs and Micros:

This function will return the checksum of the whole RAM, the device's limits of RAM, or arbitrary limits defined by the user. Select the option required. If the device limits are chosen. Then a checksum will be displayed, calculated according to the current bit mode.

For PLDs:

This function will return the JEDEC checksum.

3.5 Cyclic Redundancy Check of RAM Data

CRC

For PROMs and Micros:

Cyclic Redundancy check provides a better representation of the RAM data than a checksum as it takes account of the order of the data.

The format is the same as checksum.

For PLDs:

This function will return the Open Link Count.

3.6 Transferring DATA via the Ports

Before loading or outputting data via the ports, it is first necessary to ensure the following:

- the correct interface format is selected (see Section 2.1.5);
- the correct port is selected (see Section 2.1.4);
- and if the RS232 port is to be used, that the correct port set-up is selected (see Section 2.1.4).

when using the serial interface, it must be used with either hardware handshaking or the Xon/Xoff protocol.

See also Section 5 which contains information on pin assignments.

3.6.1 Receiving Data FROM the ports

INPUT

On pressing INPUT, three further options may be entered:

OFFSET

RAM START

RAM STOP

These are used to define where in RAM to store the data. The OFFSET value is subtracted from the address of the incoming data and the RAM ADDRESS is added on. Data beyond the RAM STOP address will be truncated.

A rotating star is displayed to indicate the data are being received.

3.6.2 Transmitting Data TO the Ports

OUTPUT

On PRESSING output, three further options may be entered:

OFFSET

RAM START

RAM STOP

OFFSET is used to generate the first transmitted address

RAM START gives the location to find the first byte of data, then the transmitted address and the RAM address are incremented until the RAM address equals RAM STOP

A rotating star is displayed to indicate that data are being transmitted.

4. Device Functions

All device functions will perform a connect test to ensure that the device is present in the socket followed by a reserve part check to ensure that the device is the correct way round. If a part is faulty it may also fail this test.

Devices should be inserted towards the front of the ZIF with pin 1 towards the rear of the machine - see diagram on Orbit 48 top panel.

! Before a device function is executed the user should ensure that the device used is the same as the device selected.

(See also Section 2.4.3 Electronic Identifier)

! Devices should be inserted into the socket with the ZIF handle up; the handle should be lowered. The handle should be raised before the part is removed.

! Devices should not be removed or socketed during a device function.

At the end of the function the display will indicate whether the function has passed or failed.

4.1 Loading the Orbit 48's RAM from a Master Device

The Master device should be placed in the socket.

LOAD

On pressing LOAD the data in the device will be copied into the Orbit 48's internal RAM.

Remove master device from the socket.

4.2 Verify

This function compares the contents of RAM with the data in the device. Some devices will be verified twice at different Vcc values as directed by the manufacturer's specification.

This will also happen if marginal verify is selected (see Section 2.4.2). If a device fails, and the function is enabled, failures will be displayed (see Section 2.5).

VERIFY

See also Section 2.5 - Displaying information about failures.

4.3 Empty

This function will check that the devices are unprogrammed. If an electrically erasable part is selected, the part can be erased during programming, so new devices may not be shipped in their empty state.

Ensure device to be checked is socketed correctly

EMPTY

See also Section 2.5 - displaying information about failures.

4.4 Program

This function initiates the automatic programming sequence.

The device is first checked with the pre-program check (see Section 2.4.1), programmed with the data in the RAM to the manufacturer's specification, verified, then security fuses may be blown if applicable (see Section 2.4.4).

Ensure the device to be programmed is socketed correctly

PROGRAM

See also Section 2.5 - Displaying information about failures.

5. Remote Operation of Orbit 48

Orbit 48 may be controlled remotely through the serial port.

The unit is put into remote mode by a key sequence in local mode (see Section 2.2). On power down, the mode of operation is remembered so it will power back up still in remote, unless the self-test fails.

To return to local, either issue the Z command or power up with the **EXIT** key pressed.

5.1 Remote Control Commands

Remote control commands are case insensitive, and so may be transmitted in either upper, lower or mixed case. Spaces and tabs are ignored. (The only exception to these rules is the remote control 'D' command). In the following table, anything printed in UPPER CASE should be sent literally, while anything in lower case represents a parameter which you should substitute with an appropriate value. Some of these commands cause Orbit 48 to transmit information back to the host, others do not. In either case, Orbit 48's response (if any) is followed immediately by a carriage-return, line feed, status-code (see Section 5.2), carriage-return, line feed, prompt (a greater than symbol >).

S0 manufacturer device Set the programmer for specified manufacturer and device. Each of the parameters consists of exactly three hexadecimal characters which can be found in the supplied device support list.

S1 format Set the I\O format. The parameter is single a ASCII character, and may be one of the following;

4(Intel hex);	5 (Motorola S-Record);
8 (Stag- hex);	9 (ASII-hex-space);
A (Stag Binary);	D (Binary);
H (POF);	I (Intel 32).

Additional formats may be added to this list by Stag at a later date.

S3 security Set the security flags. The parameter is a hexadecimal number between 00 and FF. Each bit corresponds to one security bit for the currently selected device; bit 0 orresponds to fuse 1, through to bit 6 corresponding to fuse 7. Not all devices support these features.

S4 Fill the RAM between RAM-START and RAM-STOP with the device's unprogrammed state.

SM units sets width

Set the bit- mode. The three parameters are each two - digit decimal numbers. Their meanings are as follows:

units = numbers of units per set (a unit is a contiguous region of RAM consisting of a sequence of data - words, each being " width" bits wide);

sets is the number of identical copies of each unit;

width is the bit-mode-width, measured in bits.

For 8-bit wide devices, legal combinations are:

010116 (16 BIT); 010132 (32 BIT).

SR ram_start

Set the RAM-START address to the specified hexadecimal value. Note that this operation is not carried out immediately, but instead is deferred until after the SE command is issued - therefore you MUST supply these commands in the order SR followed by SE. It is legal to omit the ram_ start parameter if you do not wish to modify it but intend to use SE.

SE ram_ stop

Set the RAM-STOP address to the specified hexadecimal value. Note that this command also makes permanent the RAM-START address specified by a previous SR command, therefore you MUST supply these commands in the order SR followed by SE. It is legal to omit the ram_stop parameter if you do not wish to modify it but intend to use SR.

SD device_start

Set the DEVICE-START address to specified hexadecimal value. Note that this command also sets the DEVICE-STOP address to: (DEVICE-START + (RAM-range / bit-mode-width)), so you MUST set the RAM-START, the RAM-STOP and the bit mode BEFORE using this command. It is legal to omit the device_start parameter if you do not wish to modify it but need to modify the device stop address (having previously modified the RAM range).

SO offset

Set the I/O offset to the specified hexadecimal value.

ST margin_mode

Set marginal testing to on (1) or off (0).

SY eid_mode

Set the electronic identifier mode to OFF (0), CHECK (1) or AUTOMATIC (2).

R0

Read the manufacturer and device code. This command outputs a six character hexadecimal number consisting of the Stag manufacturer and device codes for the currently selected device.

RI Read the interface format. This command outputs a single ASCII character representing the currently selected I/O format, which will correspond to one of the options available for the S I command.

R3 Read the security fuse setting. This command outputs a two character hexadecimal number representing the current security fuse settings. Each bit corresponds to one security bit for the currently selected device: bit 0 corresponds to fuse 1, through to bit 6 corresponding to fuse 7.

R4 Read the CRC (PROMs & Micros) or Open Link Count (PLDs). This command outputs a four character hexadecimal number.

R5 Read the RAM size. The output is a six character hexadecimal number representing the topmost RAM address available.

R6 Read the FLASH software revision number. The output consists of the ASCII string “119-” (which identifies this product as the Orbit 48) followed by two (or sometimes three) fields consisting of decimal numbers. The fields are separated by a period character (‘.’).

R7 Read the checksum. This command outputs a four character hexadecimal number.

R9 Read device description. Output consists of three fields separated by a “/” character. The first field is the maximum possible (hexadecimal) device address; the second field is the (decimal) device width measured in bits; and the last field is the empty state of the device - (‘0’ meaning all zeros; ‘1’ meaning all ones; and ‘2’ meaning unknown or indeterminate).

RM Read the current bit mode. Output consists of six digits which comprise three fixed-size fields with no separator. The first field (2 decimal digits) is the number of units per set; and the last field (2 decimal digits) is the bit-mode-width measured in bits – see also SM.

RR Read the current RAM-START address. Output consists of six hexadecimal characters.

RE	Read the current RAM-STOP address. Output consists of six hexadecimal characters.
RD	Read the current DEVICE-START address. Output consists of six hexadecimal characters.
RO	Read the current I/O offset. Output consists of eight hexadecimal characters.
RT	Read the current marginal-test setting. Output is ASCII '0' for off, or '1' for on.
RY	Read current electronic identifier setting. Output is ASCII '0' for off, '1' for Check or '2' for Automatic.
RP	Read FLASH PROM size. Output is a six character hexadecimal number representing the amount of FLASH PROM currently installed in your machine.
P0	Program device with pre-program illegal bit check.
P1	Program devices with no pre-program check.
L	Load device.
E	Empty check device.
V	Verify device.
I	Input data from the serial I/O port using the currently selected I/O format into the Orbit 48's RAM, using the currently selected RAM-START, RAM-STOP and I/O-OFFSET settings.
O	Output data from the Orbit 48's RAM to the currently selected port using the currently selected I/O format, and using the currently selected RAM-START, RAM-STOP and I/O - OFFSET settings.

F pattern	Fill RAM between the current RAM-START and RAM-STOP limits using the specified pattern. The pattern must consist of either 2, 4 or 8 hexadecimal characters.
H number	Sound the horn (beeper) the specified number of times. The single parameter should be a decimal number between 1 and 20.
Dstring	Display a string on Orbit 48's LCD. This is the only command for which spaces and tabs are not ignored and, for which, case is important. There should be no space between the D and the first character of the string. The string may not contain line feeds or carriage-returns - but it CAN however contain ANS1 X3.64-1979 console escape sequences (so new line can be simulated by transmitting the two bytes $9B followed by $45).
K	Wait for any key on the Orbit 48's keypad to be pressed. There is no way of detecting which key is pressed.
Z	Exit remote control mode.
M0	Emulation Off
M1	Emulation On
M	Read Emulator Status

5.2 Status Codes

Status codes returned by the Orbit 48 consists of two hexadecimal characters.

The following responses may be obtained:

00 Command executed successfully

01 No Blow
Device failed to program.

02 Device failed to verify.

04 Device failed empty test.

05 Device failed connect test.

06 Device found to be reversed or faulty.

08 WARNING: Command executed successfully, but something minor Went wrong.

09 Security bit(s) failed to program.

0A Command parameters incorrect.

0B Error in inputting data

0D RAM failure

11 Wrong part. Electronic identifier Check failed.

12 Mismatched parts

13 Illegal or out of range address.

14 FLASH fail. Software in FLASH PROM has become corrupted.

15 No signature. Device could not be recognised by electronic identifier.

IC Out of memory.

IE Function aborted.

IF Unrecognised command or syntax error.

5.3 Pinouts for Serial Port Connector

Pin No.	Signal Name		Comment
1	DCD	carrier detect	not used but pulled high
2	RXR	receive data	
3	TXD	transmit data	
4	DTR	data terminal ready	
5	SG	signal ground	
6	DSR	data set ready	
7	RTS	request to send	
8	CTS	clear to send	
9	RI	ring indicator	not used

5.4 Parallel Pinout

Pin	Name	Function
1	STB	Data Strobe, valid data present when low.
2	D0	LS data bit.
3	D1	
4	D2	
5	D3	
6	D4	
7	D5	
8	D6	
9	D7	MS data bit.
10	ACK	Data acknowledged when low.
11	BUSY	Receiver not ready when high.
12	PE	Not used.
13	SLCT	Not used.
14	AFD	Not used.
15	ERR	Not used.
16	INIT	Not used.
17	SLIN	Not used.