

DRAWN	5	28.09.84	CIS 592				
CIRCUITPLAN	4	30.07.84	CIS 555				
TRACED	3	13.03.84	CIS 481				
CHECKED	2	27.01.84	CIS 460	7	12.04.85	CIS 703	
APPROVED	1	03.01.84	CIS 451	6	06.12.84	CIS 638	
ISS		DATE	CHANGE No.	APP	ISS	DATE	CHANGE No.

LIMITS UNLESS OTHERWISE STATED

1 DECIMAL PLACE +/- 1.0mm THREADS TO BS 1580
 2 DECIMAL PLACES +/- 0.4mm DIMENSIONS APPLY.
 3 DECIMAL PLACES +/- 0.1mm AFTER FINISHING
 ANGLES +/- 1/2° REMOVE ALL BURRS AND SHARP EDGES

DIMENSIONS IN	SCALE
MATERIAL	
FINISH	

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stag PP39

TITLE MAIN SYSTEM PCB CIRCUIT DIAGRAM

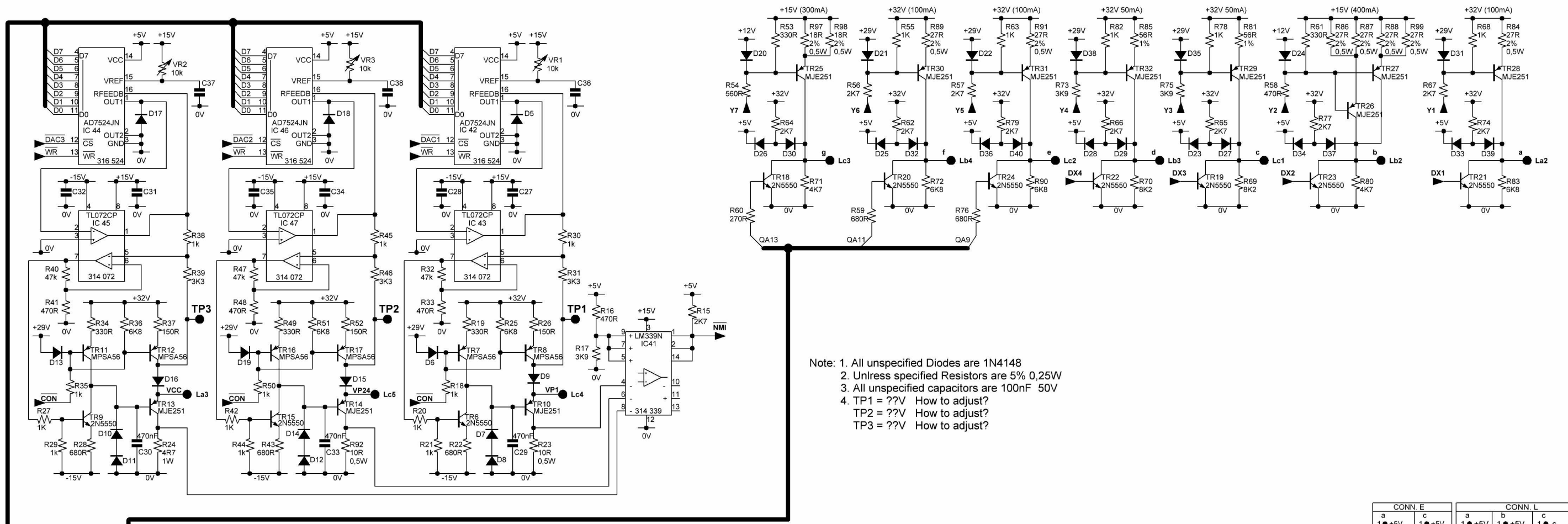
100 - 0020

7.1

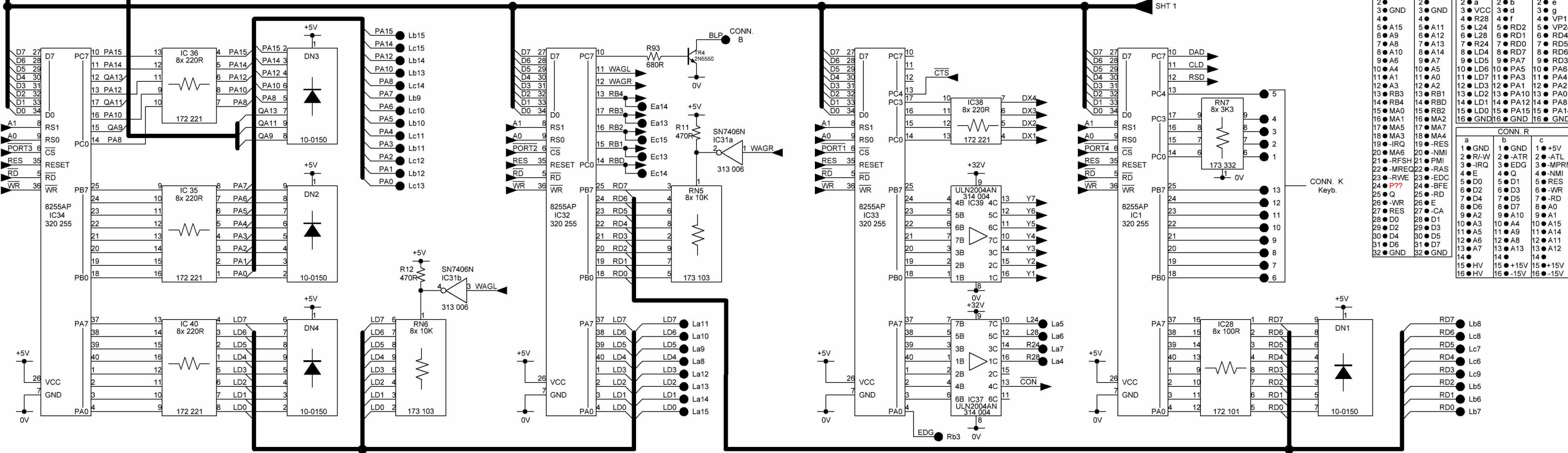
SHT 1 OF 2

CONN E			CONN L			CONN R		
a	b	c	a	b	c	a	b	c
1 ● +5V	1 ● +5V	1 ● c	1 ● +5V	1 ● +5V	1 ● c	1 ● GND	1 ● GND	1 ● +5V
2 ● GND	2 ● a	2 ● e	2 ● a	2 ● b	2 ● e	2 ● R/W	2 ● ATX	2 ● ATL
3 ● GND	3 ● GND	3 ● G	3 ● VCC	3 ● d	3 ● g	3 ● -IRQ	3 ● EDG	3 ● MPRM
4 ● A15	4 ● R28	4 ● F	4 ● R28	4 ● F	4 ● VP1	4 ● E	4 ● RAS	4 ● NMI
5 ● A11	5 ● L24	5 ● RD2	5 ● L24	5 ● RD2	5 ● VP24	5 ● D0	5 ● D1	5 ● RES
6 ● A8	6 ● A12	6 ● RD1	6 ● L28	6 ● RD1	6 ● RD4	6 ● D2	6 ● D3	6 ● -WR
7 ● A9	7 ● A13	7 ● RD0	7 ● R24	7 ● RD0	7 ● RD5	7 ● Q	7 ● -RD	7 ● -RD
8 ● A10	8 ● A14	8 ● RD7	8 ● LD4	8 ● RD7	8 ● RD6	8 ● -WR	8 ● CA	8 ● D0
9 ● A6	9 ● A7	9 ● PA7	9 ● LD5	9 ● PA7	9 ● RD3	9 ● RES	9 ● -R	9 ● A2
10 ● A4	10 ● A5	10 ● PA5	10 ● LD6	10 ● PA5	10 ● PA4	10 ● D4	10 ● D5	10 ● A1
11 ● A1	11 ● A0	11 ● PA3	11 ● LD7	11 ● PA3	11 ● PA4	11 ● D6	11 ● D7	11 ● A14
12 ● A3	12 ● A2	12 ● PA2	12 ● LD3	12 ● PA2	12 ● PA2	12 ● D8	12 ● D9	12 ● A11
13 ● RB3	13 ● RB1	13 ● PA10	13 ● LD2	13 ● PA10	13 ● PA8	13 ● D10	13 ● D11	13 ● A10
14 ● RB4	14 ● RB2	14 ● PA8	14 ● LD1	14 ● PA12	14 ● PA8	14 ● D12	14 ● D13	14 ● A9
15 ● MA0	15 ● RB2	15 ● PA15	15 ● LD0	15 ● PA15	15 ● PA14	15 ● D14	15 ● D15	15 ● A8
16 ● MA1	16 ● MA2	16 ● GND	16 ● GND	16 ● GND	16 ● GND	16 ● D16	16 ● D17	16 ● A7
17 ● MA5	17 ● MA7					17 ● D18	17 ● D19	17 ● A6
18 ● MA3	18 ● MA4					18 ● D20	18 ● D21	18 ● A5
19 ● -IRQ	19 ● -RES					19 ● D22	19 ● D23	19 ● A4
20 ● MA6	20 ● -NMI					20 ● D24	20 ● D25	20 ● A3
21 ● -RFSH	21 ● PMI					21 ● D26	21 ● D27	21 ● A2
22 ● -MREQ	22 ● -RAS					22 ● D28	22 ● D29	22 ● A1
23 ● -RWE	23 ● -EDC					23 ● D30	23 ● D31	23 ● A0
24 ● P??	24 ● -BFE					24 ● D32	24 ● D33	
25 ● Q	25 ● -RD							
26 ● -WR	26 ● E							
27 ● RES	27 ● CA							
28 ● D0	28 ● D1							
29 ● D2	29 ● D3							
30 ● D4	30 ● D5							
31 ● D6	31 ● D7							
32 ● GND	32 ● GND							

Note: 1. All unspecified Diodes are 1N4148
 2. All unspecified capacitors are 100nF 50V



- Note: 1. All unspecified Diodes are 1N4148
 2. Unless specified Resistors are 5% 0,25W
 3. All unspecified capacitors are 100nF 50V
 4. TP1 = ??V How to adjust?
 TP2 = ??V How to adjust?
 TP3 = ??V How to adjust?



CONN E			CONN L		
a	b	c	a	b	c
1 ● +5V	1 ● +5V	1 ● c	1 ● +5V	1 ● +5V	1 ● c
2 ●	2 ●	2 ● e	2 ●	2 ●	2 ● e
3 ● GND	3 ● GND	3 ● VCC	3 ● VCC	3 ● d	3 ● g
4 ●	4 ● R28	4 ● f	4 ● R28	4 ● f	4 ● VP1
5 ● A15	5 ● A11	5 ● L24	5 ● RD2	5 ● VP24	
6 ● A9	6 ● A12	6 ● L28	6 ● RD1	6 ● RD4	
7 ● A8	7 ● A13	7 ● R24	7 ● RD0	7 ● RD5	
8 ● A10	8 ● A14	8 ● LD4	8 ● RD7	8 ● RD6	
9 ● A6	9 ● A7	9 ● LD5	9 ● PA7	9 ● RD3	
10 ● A4	10 ● A5	10 ● LD6	10 ● PA5	10 ● PA6	
11 ● A1	11 ● A0	11 ● LD7	11 ● PA3	11 ● PA4	
12 ● A3	12 ● A2	12 ● LD3	12 ● PA1	12 ● PA2	
13 ● RB3	13 ● RB1	13 ● LD2	13 ● PA10	13 ● PA0	
14 ● RB4	14 ● RBD	14 ● LD1	14 ● PA12	14 ● PA8	
15 ● MA0	15 ● RB2	15 ● LD0	15 ● PA15	15 ● PA14	
16 ● MA1	16 ● MA2	16 ● GND16	16 ● GND16	16 ● GND	
17 ● MA5	17 ● MA7				
18 ● MA3	18 ● MA4				
19 ● IRQ	19 ● -RES				
20 ● MA6	20 ● -NMI				
21 ● -RFSH	21 ● PMI				
22 ● -MREQ	22 ● -RAS				
23 ● -RWE	23 ● -EDC				
24 ● P??	24 ● -BFE				
25 ● Q	25 ● -RD				
26 ● -WR	26 ● E				
27 ● RES	27 ● -CA				
28 ● D0	28 ● D1				
29 ● D2	29 ● D3				
30 ● D4	30 ● D5				
31 ● D6	31 ● D7				
32 ● GND	32 ● GND				

DRAWN	CIRCUITPLAN	5	16.12.84	CIS 638			
TRACED		4	12.07.84	CIS 555			
CHECKED		3	13.03.84	CIS 481			
		2	27.01.84	CIS 460			
APPROVED		1	13.01.84	CIS 451	6	12.04.85	CIS 703
ISS	DATE	CHANGE No.	APP	ISS	DATE	CHANGE No.	APP

LIMITS UNLESS OTHERWISE STATED

1 DECIMAL PLACE +/- 1.0mm
 2 DECIMAL PLACES +/- 0.4mm
 3 DECIMAL PLACES +/- 0.1mm

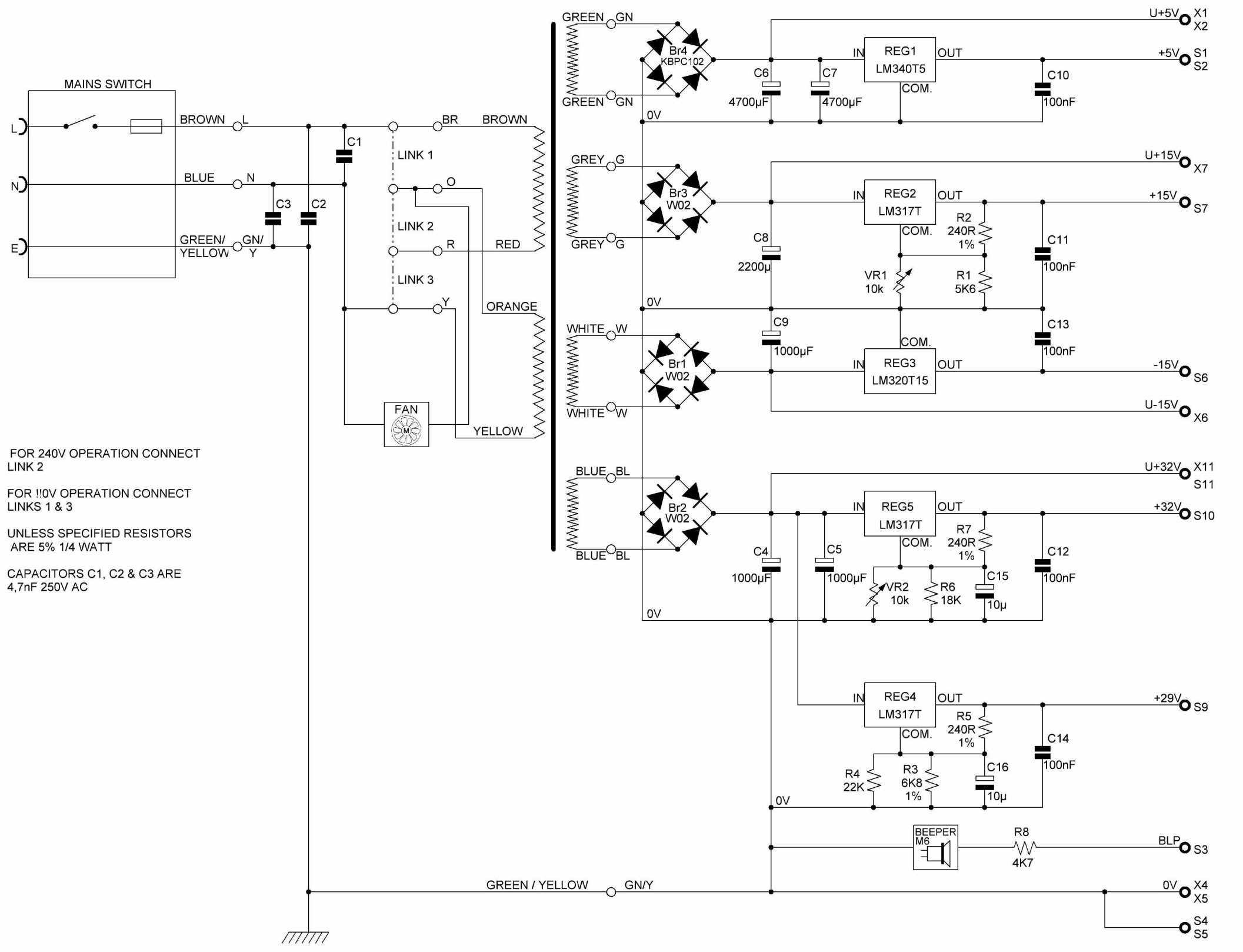
THREADS TO BS 1580
 DIMENSIONS APPLY.
 AFTER FINISHING
 ANGLES +/- 1/2° REMOVE ALL BURRS AND SHARP EDGES

DIMENSIONS IN	SCALE
MATERIAL	
FINISH	

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TITLE MAIN SYSTEM PCB CIRCUIT DIAGRAM

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- NOTE 1 FOR 240V OPERATION CONNECT LINK 2
- 2 FOR 110V OPERATION CONNECT LINKS 1 & 3
- 3 UNLESS SPECIFIED RESISTORS ARE 5% 1/4 WATT
- 4 CAPACITORS C1, C2 & C3 ARE 4,7nF 250V AC

CONN. S	CONN. X
1 • 5V	1 • U+5V
2 • 5V	2 • U+5V
3 • BLP	3 • NC
4 • 0V	4 • 0V
5 • 0V	5 • 0V
6 • -15V	6 • U-15V
7 • +15V	7 • U+15V
8 • NC	8 • NC
9 • +29V	9 • NC
10 • +32V	10 • NC
11 • U+32V	11 • U+32V

DRAWN							
CIRCUITPLAN							
TRACED							
CHECKED	2	27.06.85	CIS 752				
	1	27.07.84	CIS 558				
APPROVED	ISS	DATE	CHANGE No.	APP	ISS	DATE	CHANGE No.

LIMITS UNLESS OTHERWISE STATED

1 DECIMAL PLACE +/- 1.0mm THREADS TO BS 1580

2 DECIMAL PLACES +/- 0.4mm DIMENSIONS APPLY.

3 DECIMAL PLACES +/- 0.1mm AFTER FINISHING

ANGLES +/- 1/2° REMOVE ALL BURRS AND SHARP EDGES

DIMENSIONS IN	SCALE
MATERIAL	
FINISH	

stag PP39

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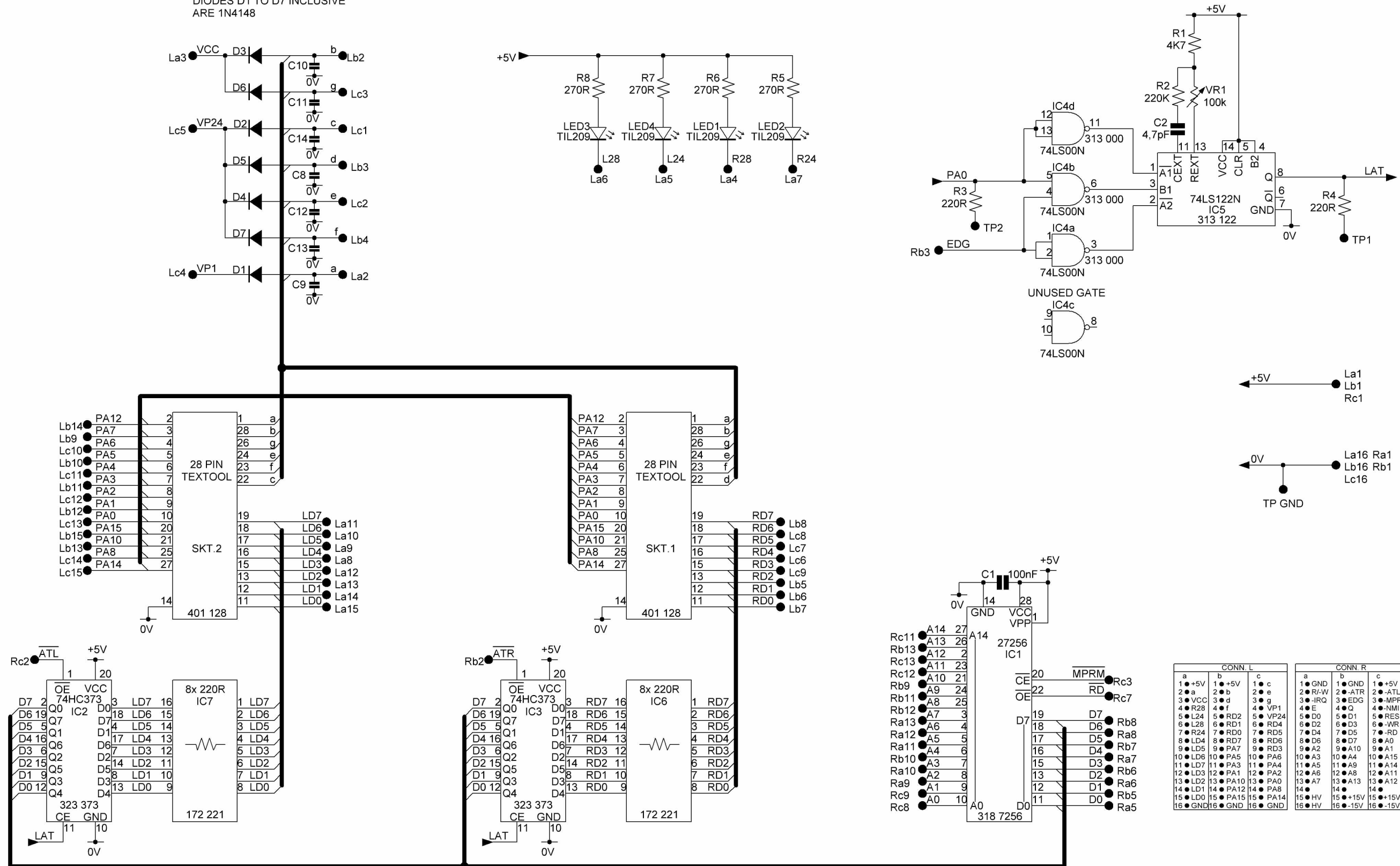
TITLE: POWER SUPPLY PCB CIRCUIT DIAGRAM

100 - 0030

7.2

SHT 1 OF 1

NOTE CAPACITORS C8 TO C14 INCLUSIVE
ARE 10nF 50V
DIODES D1 TO D7 INCLUSIVE
ARE 1N4148



CONN L			CONN R		
a	b	c	a	b	c
1 • +5V	1 • +5V	1 • c	1 • GND	1 • GND	1 • +5V
2 • a	2 • b	2 • e	2 • R/W	2 • -ATR	2 • -ATL
3 • VCC	3 • d	3 • g	3 • -IRQ	3 • EDG	3 • -MPRM
4 • R28	4 • f	4 • VP1	4 • E	4 • Q	4 • -NMI
5 • L24	5 • RD2	5 • VP24	5 • D0	5 • D1	5 • RES
6 • L28	6 • RD1	6 • RD4	6 • D2	6 • D3	6 • -WR
7 • R24	7 • RD0	7 • RD5	7 • D4	7 • D5	7 • -RD
8 • LD4	8 • RD7	8 • RD6	8 • D6	8 • D7	8 • A0
9 • LD5	9 • PA7	9 • RD3	9 • A2	9 • A10	9 • A1
10 • LD6	10 • PA5	10 • PA6	10 • A3	10 • A4	10 • A15
11 • LD7	11 • PA3	11 • PA4	11 • A5	11 • A9	11 • A14
12 • LD3	12 • PA1	12 • PA2	12 • A6	12 • A8	12 • A11
13 • LD2	13 • PA10	13 • PA8	13 • A7	13 • A13	13 • A12
14 • LD1	14 • PA12	14 • PA8	14 • A8	14 • A	14 • A
15 • LD0	15 • PA15	15 • PA14	15 • HV	15 • +15V	15 • +15V
16 • GND16	16 • GND	16 • GND	16 • HV	16 • -15V	16 • -15V

DRAWN							
CIRCUITPLAN							
TRACED							
CHECKED	2	18.09.84	CIS 586				
APPROVED	1	19.09.83	CIS416				
	ISS	DATE	CHANGE No.	APP	ISS	DATE	CHANGE No.

LIMITS UNLESS OTHERWISE STATED

1 DECIMAL PLACE +/- 1.0mm | THREADS TO BS 1580
 2 DECIMAL PLACES +/- 0.4mm | DIMENSIONS APPLY.
 3 DECIMAL PLACES +/- 0.1mm | AFTER FINISHING
 ANGLES +/- 1/2° REMOVE ALL BURRS AND SHARP EDGES

DIMENSIONS IN	SCALE
MATERIAL	
FINISH	

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Please send comments and errors to emesstec@t-online.de

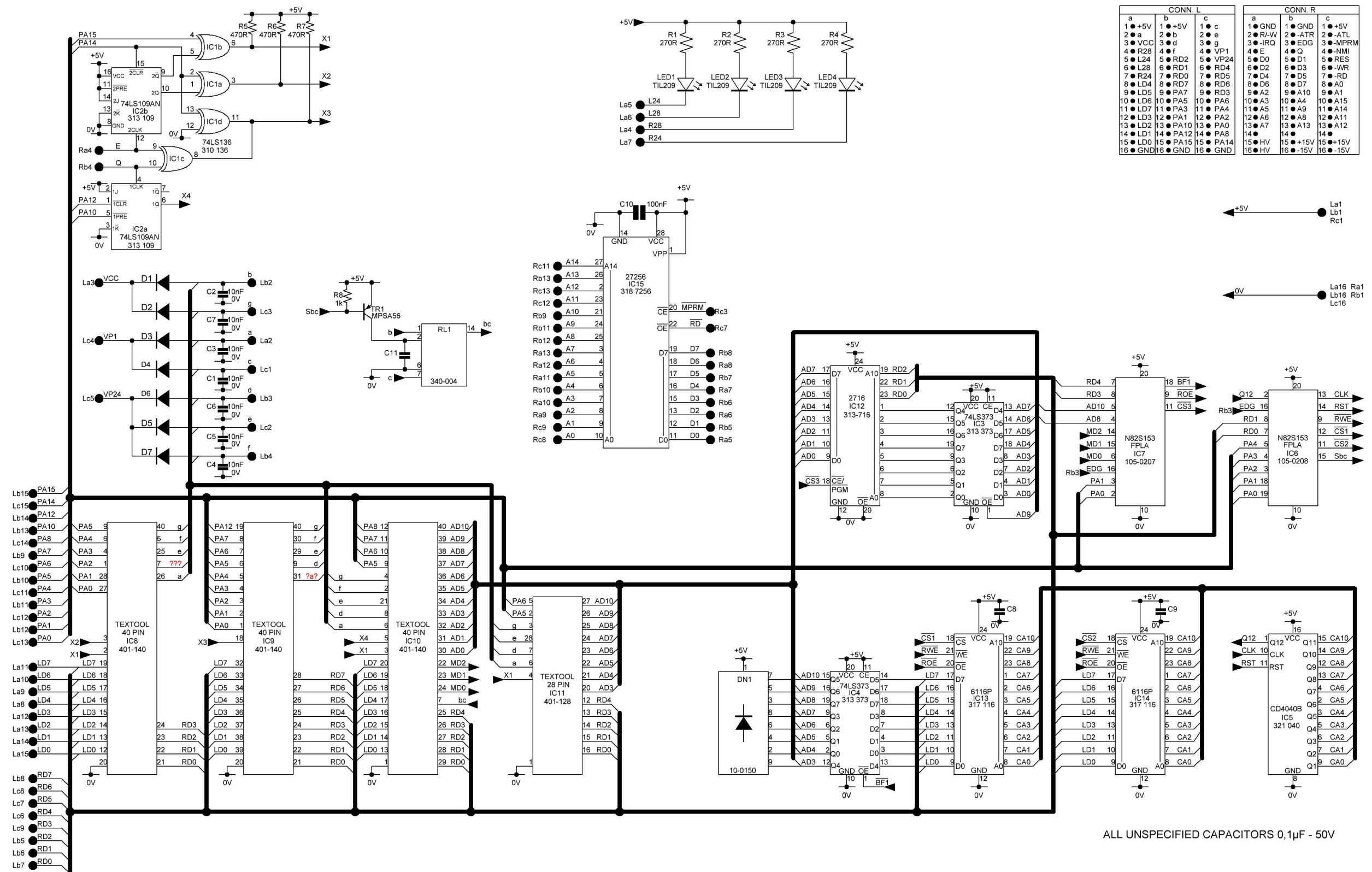
stag PP39

PROM MODULE 39M100 PCB CIRCUIT DIAGRAM

105 - 0000

8.1.4

SHT 1 OF 1



CONN. L			CONN. R		
a	b	c	a	b	c
1 ● +5V	1 ● +5V	1 ● c	1 ● GND	1 ● GND	1 ● +5V
2 ● a	2 ● b	2 ● e	2 ● R/W	2 ● -ATR	2 ● -ATL
3 ● VCC	3 ● d	3 ● g	3 ● -IRO	3 ● EDG	3 ● -MPRM
4 ● R28	4 ● f	4 ● VP1	4 ● E	4 ● Q	4 ● -NMI
5 ● L24	5 ● RD2	5 ● VP24	5 ● D0	5 ● D1	5 ● RES
6 ● L28	6 ● RD1	6 ● RD4	6 ● D2	6 ● D3	6 ● -WR
7 ● R24	7 ● RD0	7 ● RD5	7 ● D4	7 ● D5	7 ● -RD
8 ● LD4	8 ● RD7	8 ● RD6	8 ● D6	8 ● D7	8 ● A0
9 ● LD5	9 ● PA7	9 ● RD3	9 ● A2	9 ● A10	9 ● A1
10 ● LD6	10 ● PA5	10 ● PA6	10 ● A3	10 ● A4	10 ● A15
11 ● LD7	11 ● PA3	11 ● PA4	11 ● A5	11 ● A9	11 ● A11
12 ● LD3	12 ● PA1	12 ● PA2	12 ● A6	12 ● A8	12 ● A14
13 ● LD2	13 ● PA10	13 ● PA0	13 ● A7	13 ● A13	13 ● A12
14 ● LD1	14 ● PA12	14 ● PA8	14 ● ●	14 ● ●	14 ● ●
15 ● LD0	15 ● PA15	15 ● PA14	15 ● ●	15 ● +15V	15 ● +15V
16 ● GND	16 ● GND	16 ● GND	16 ● HV	16 ● -15V	16 ● -15V

ALL UNSPECIFIED CAPACITORS 0,1µF - 50V

DRAWN							
CIRCUITPLAN							
TRACED							
CHECKED							
APPROVED	1	19.10.84	CIS609				
	ISS	DATE	CHANGE No.	APP	ISS	DATE	CHANGE No.

LIMITS UNLESS OTHERWISE STATED
 1 DECIMAL PLACE +/- 1.0mm THREADS TO BS 1580
 2 DECIMAL PLACES +/- 0.4mm DIMENSIONS APPLY.
 3 DECIMAL PLACES +/- 0.1mm AFTER FINISHING
 ANGLES +/- 1/2° REMOVE ALL BURRS AND SHARP EDGES

DIMENSIONS IN	SCALE
MATERIAL	
FINISH	

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TITLE	PROM MODULE 39M200 PCB CIRCUIT DIAGRAM
	105 - 0210
	8.2.3
	SHT 1 OF 1